

FIG.1

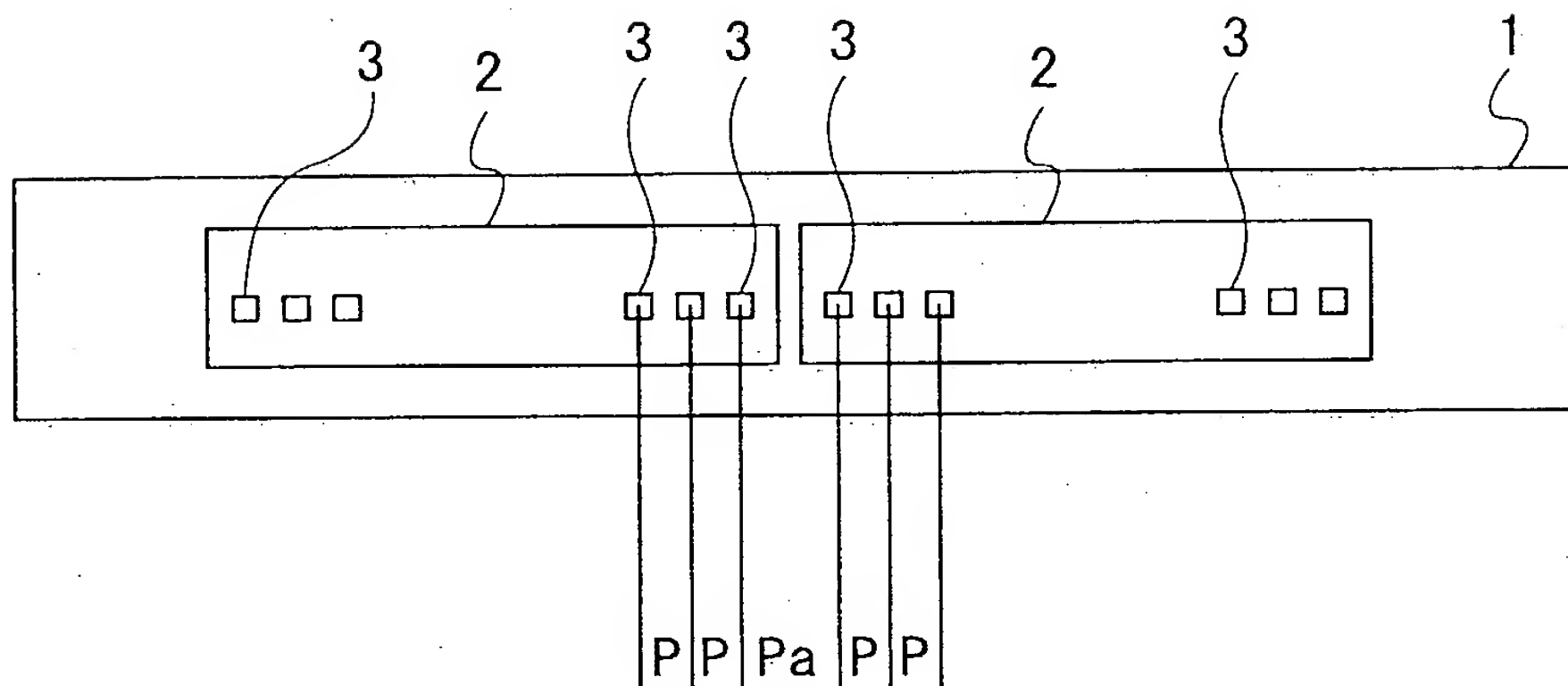


FIG.2

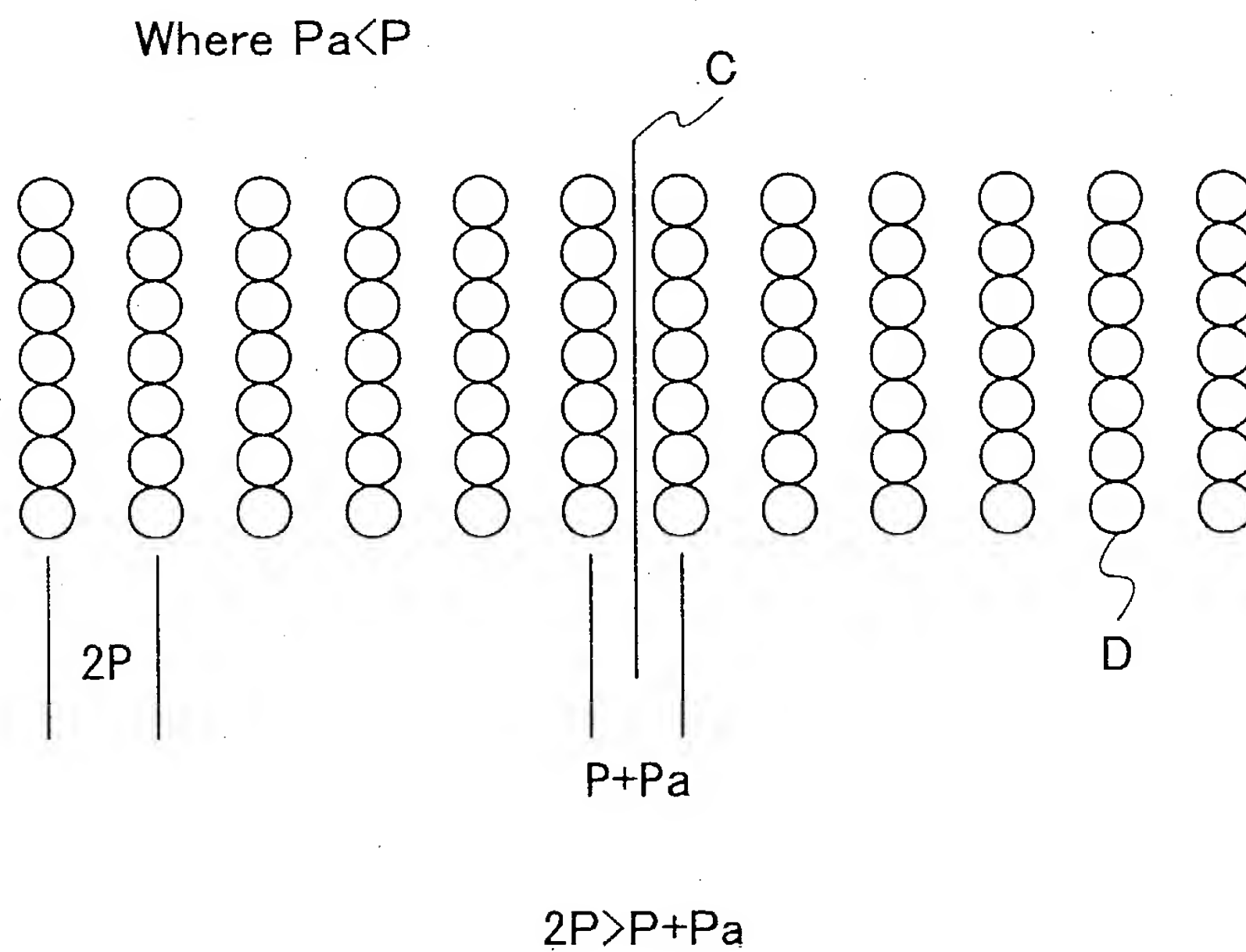


FIG.3

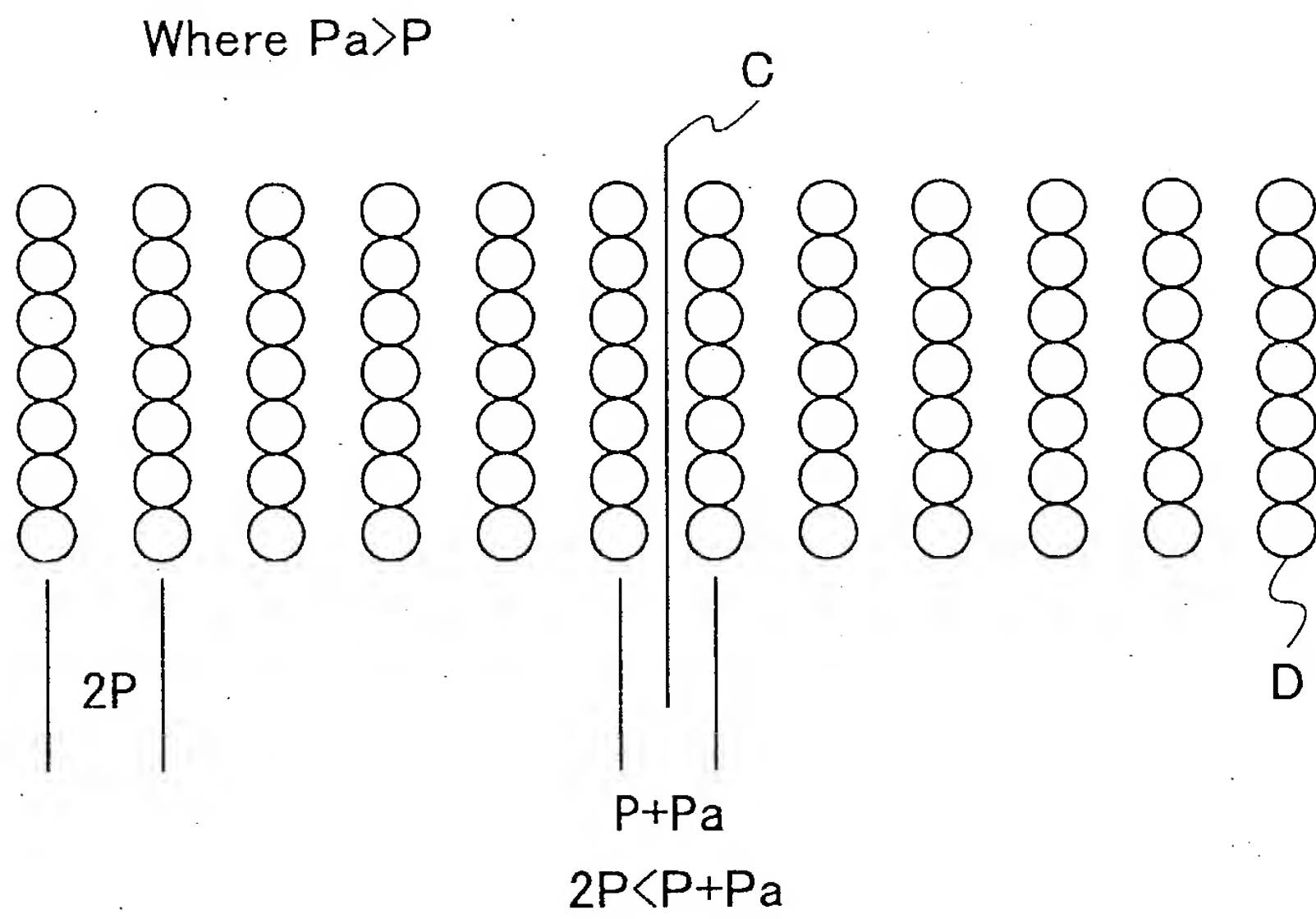


FIG.4

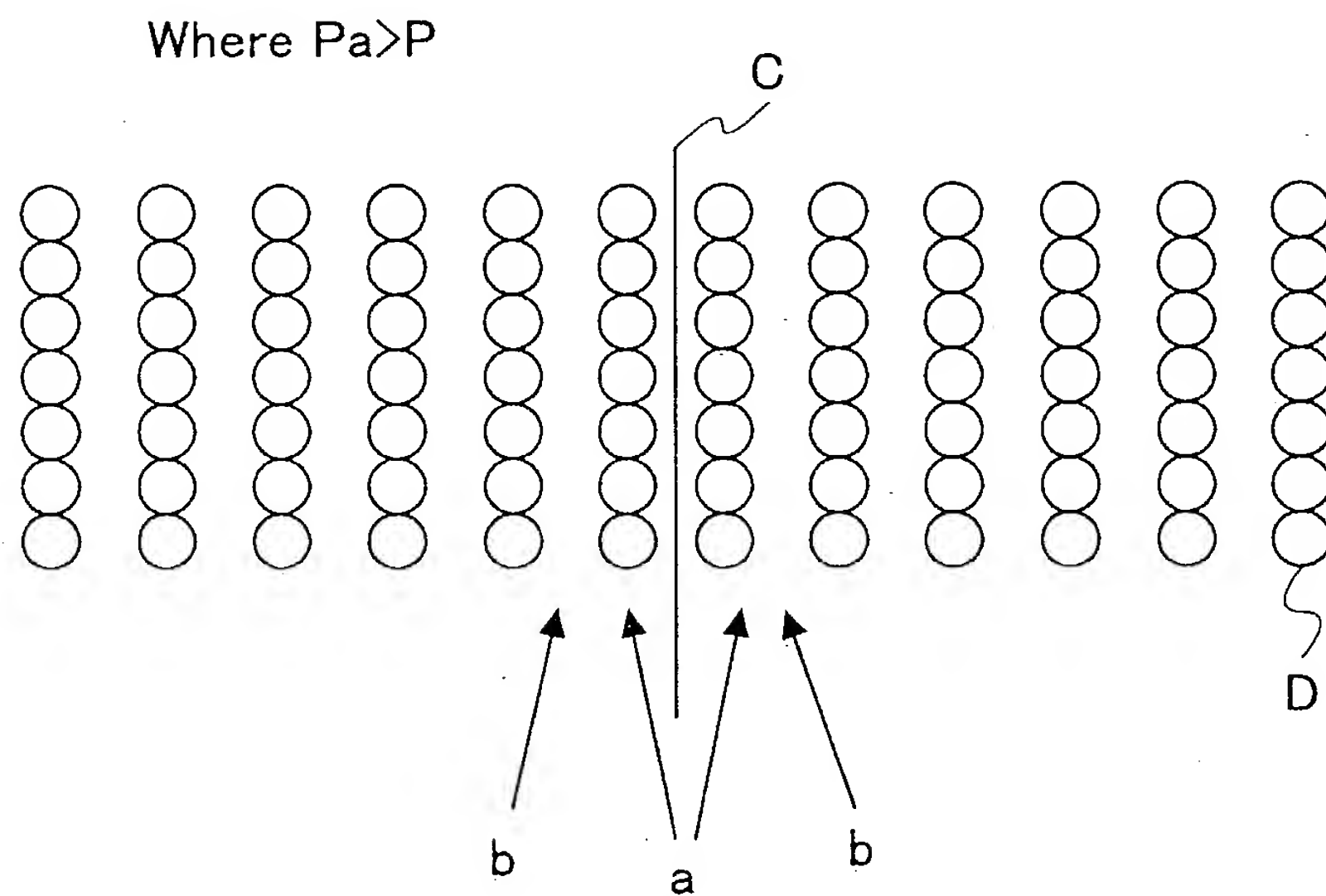


FIG.5

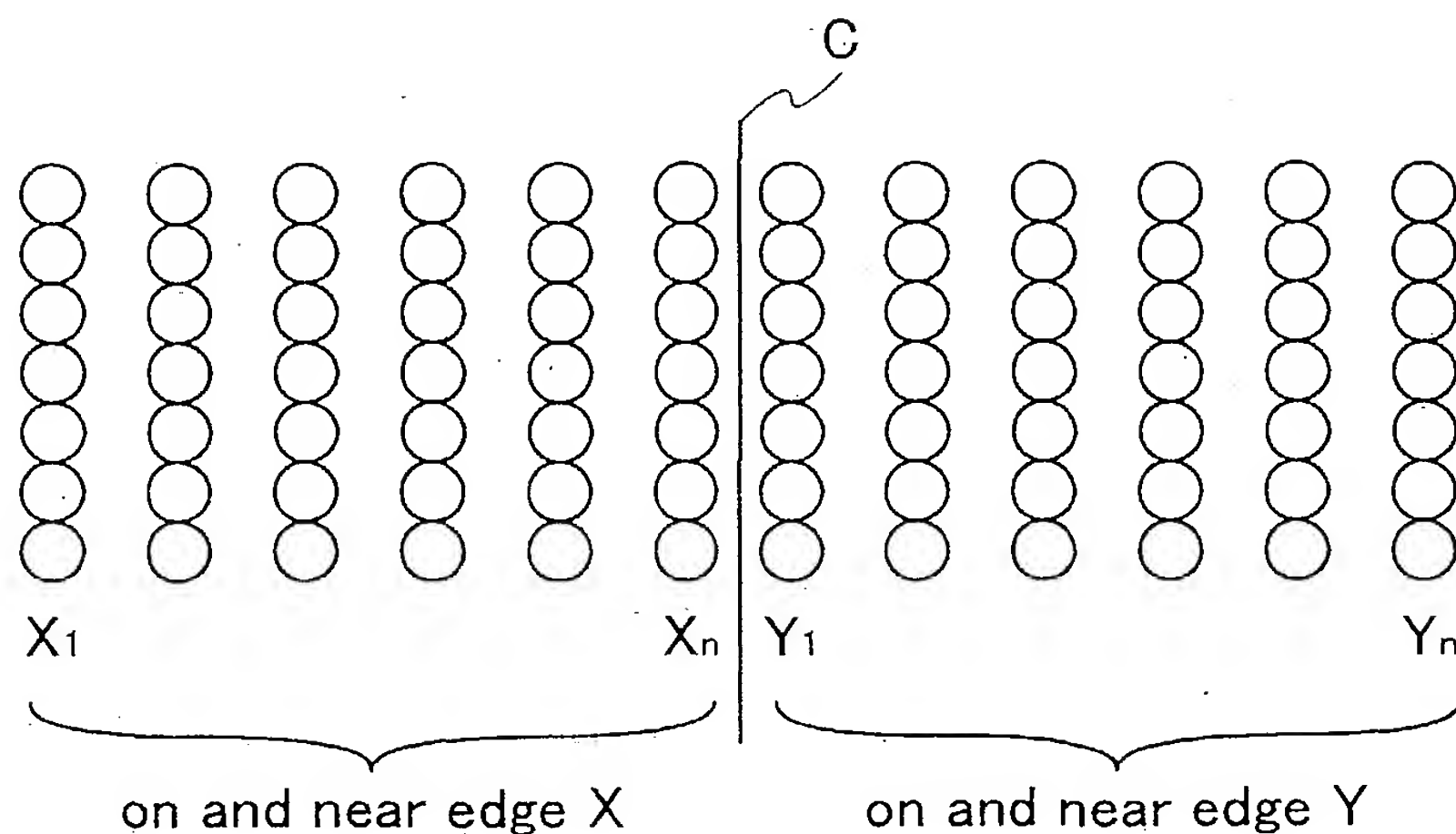


FIG.6

Where $P_a > P$

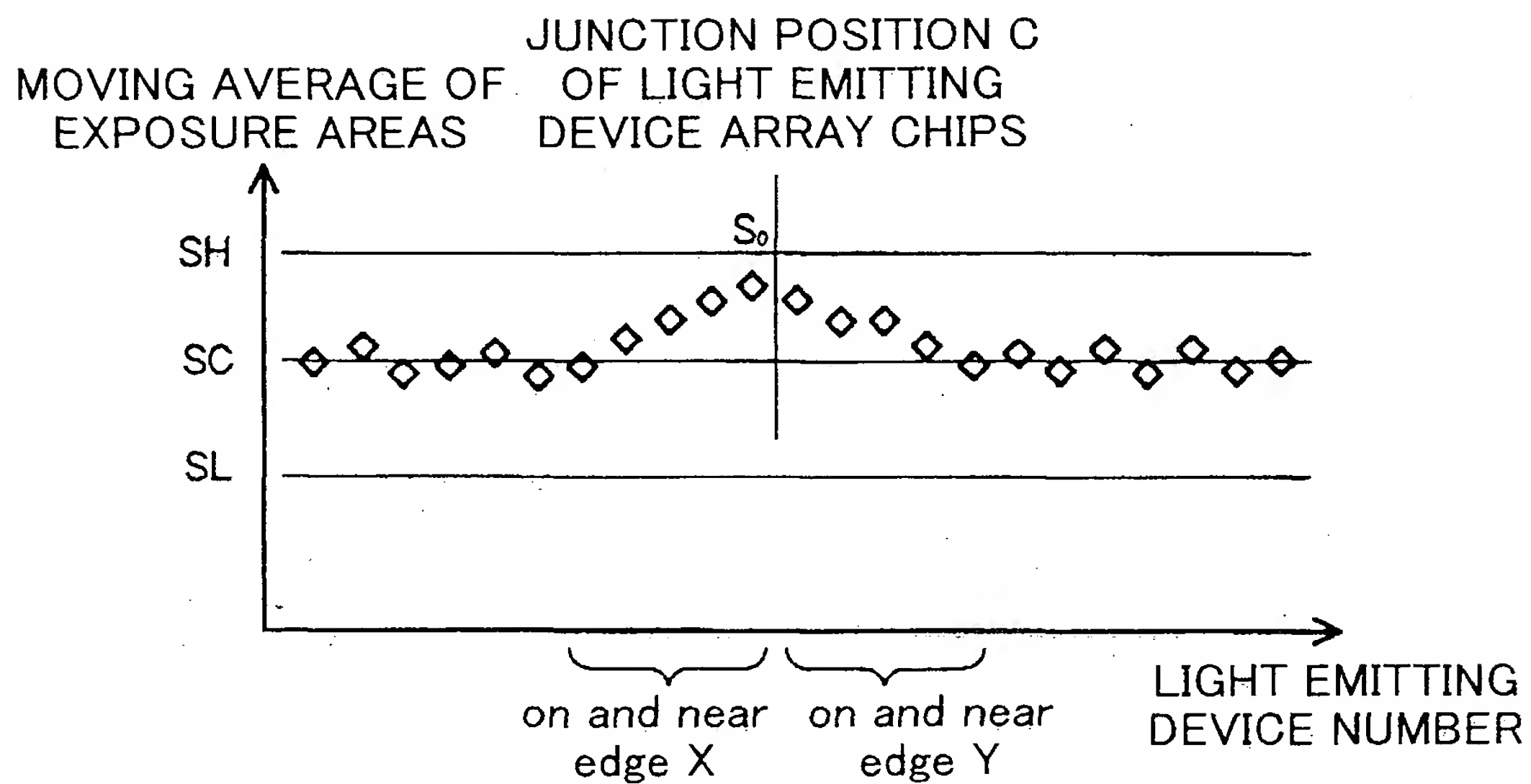


FIG.7

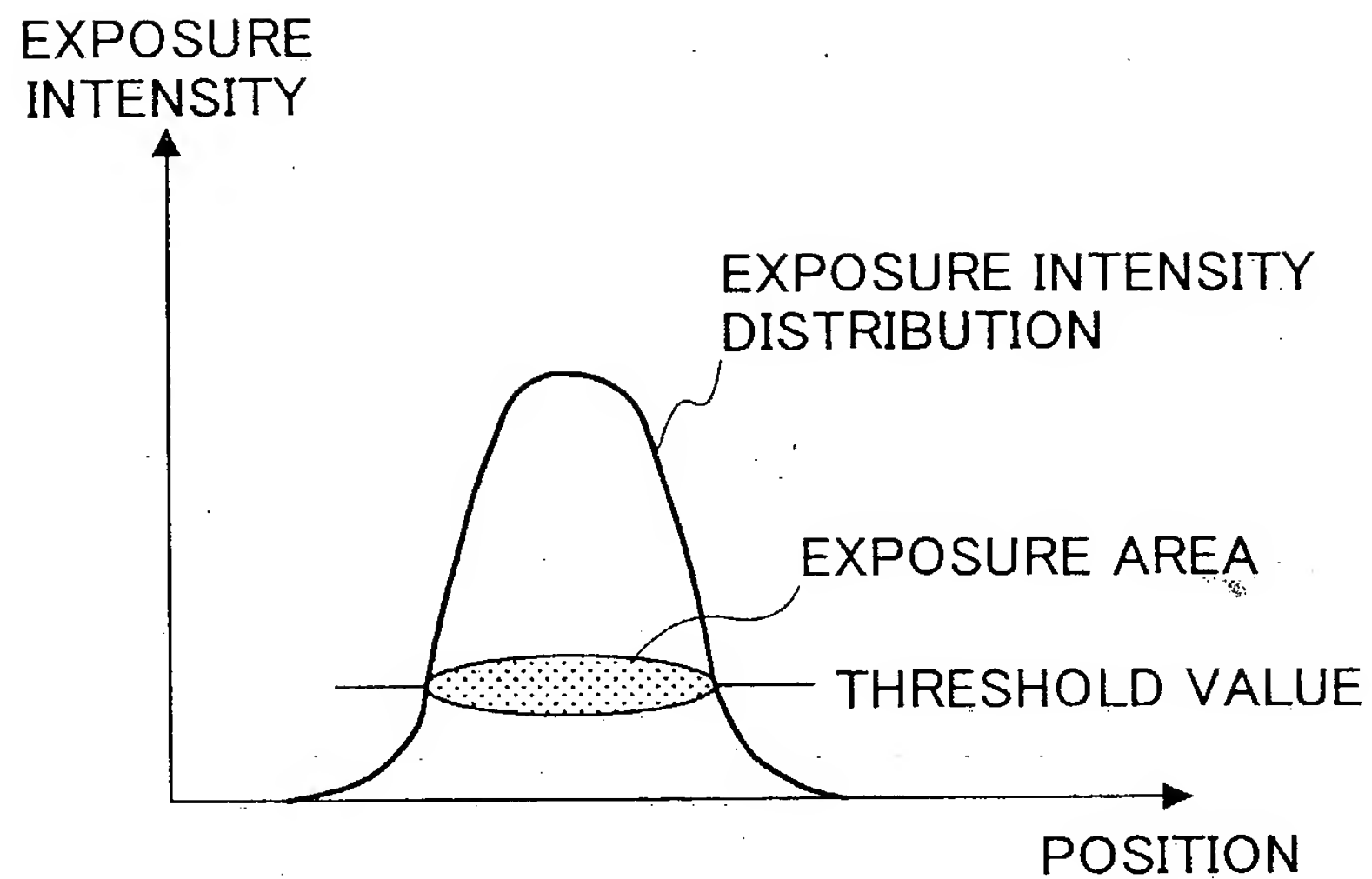


FIG.8

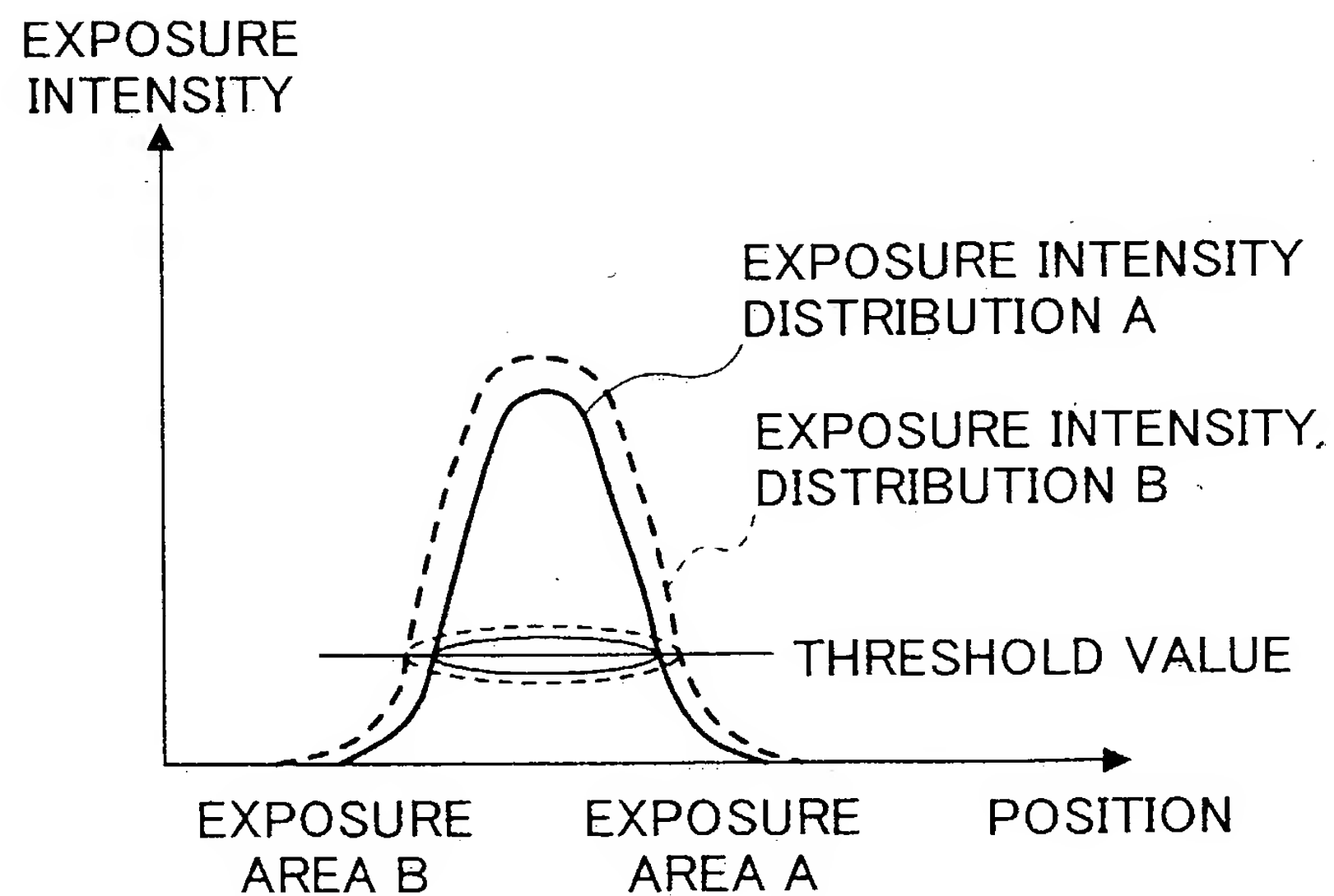


FIG.9

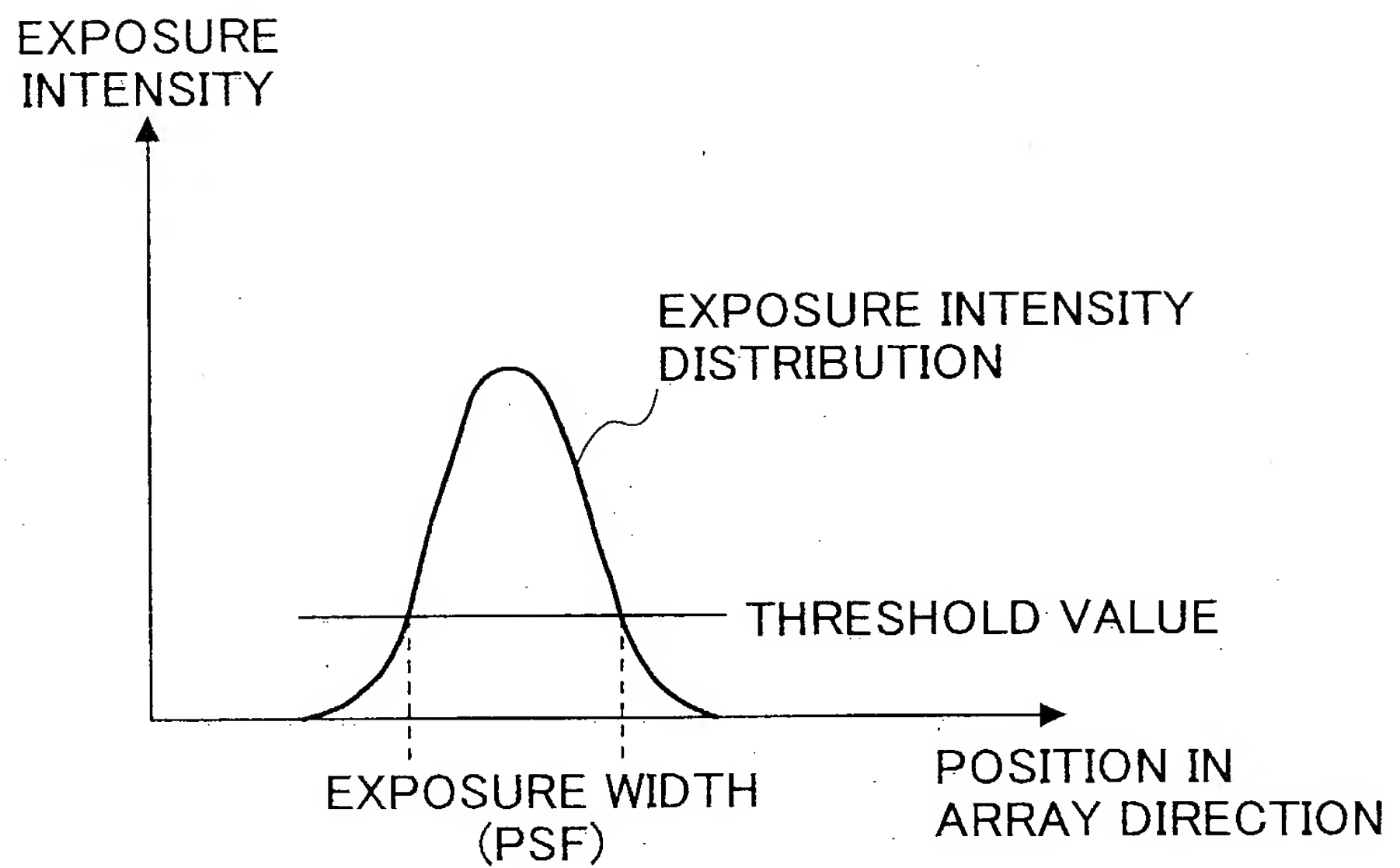


FIG.10

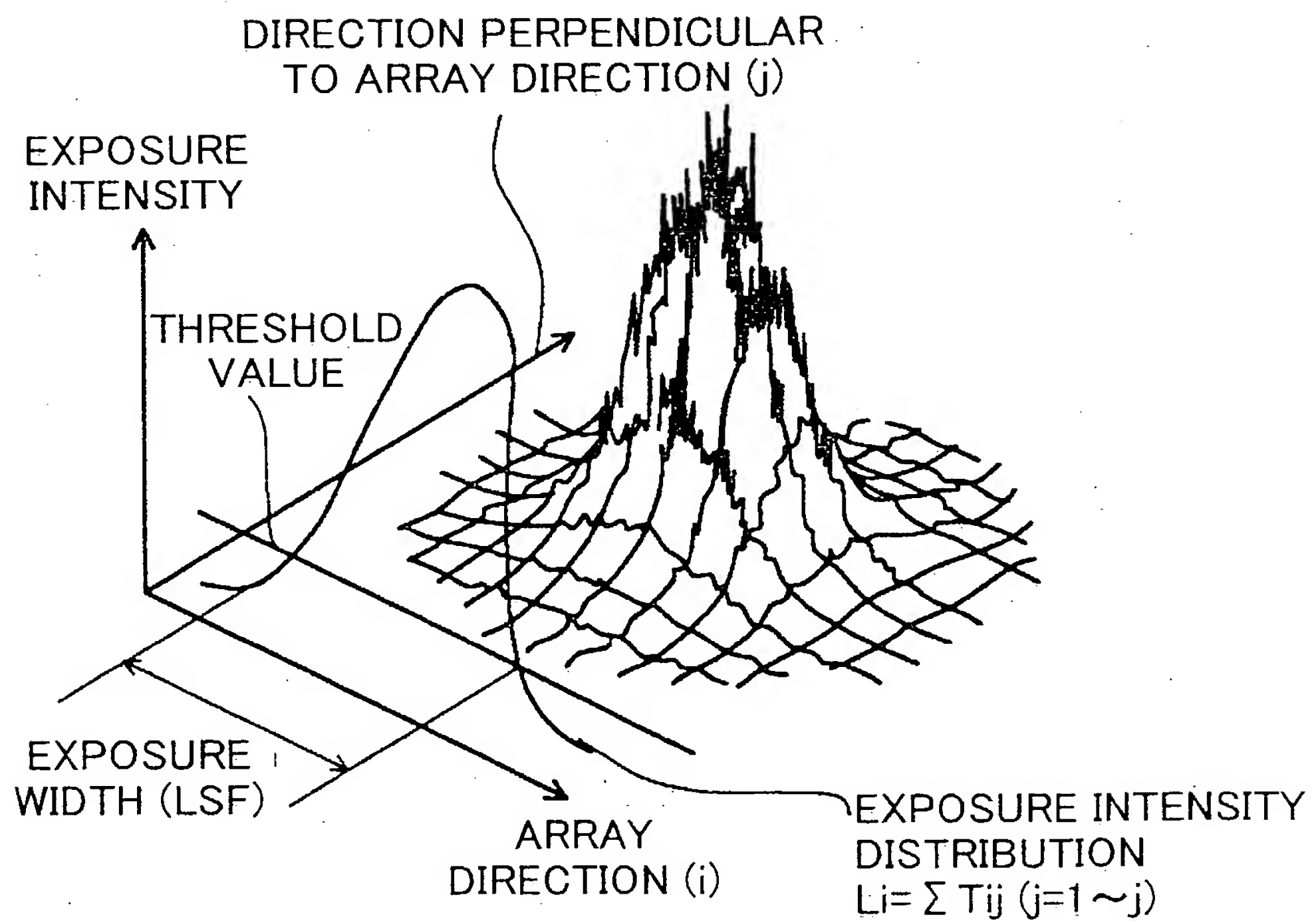


FIG.11

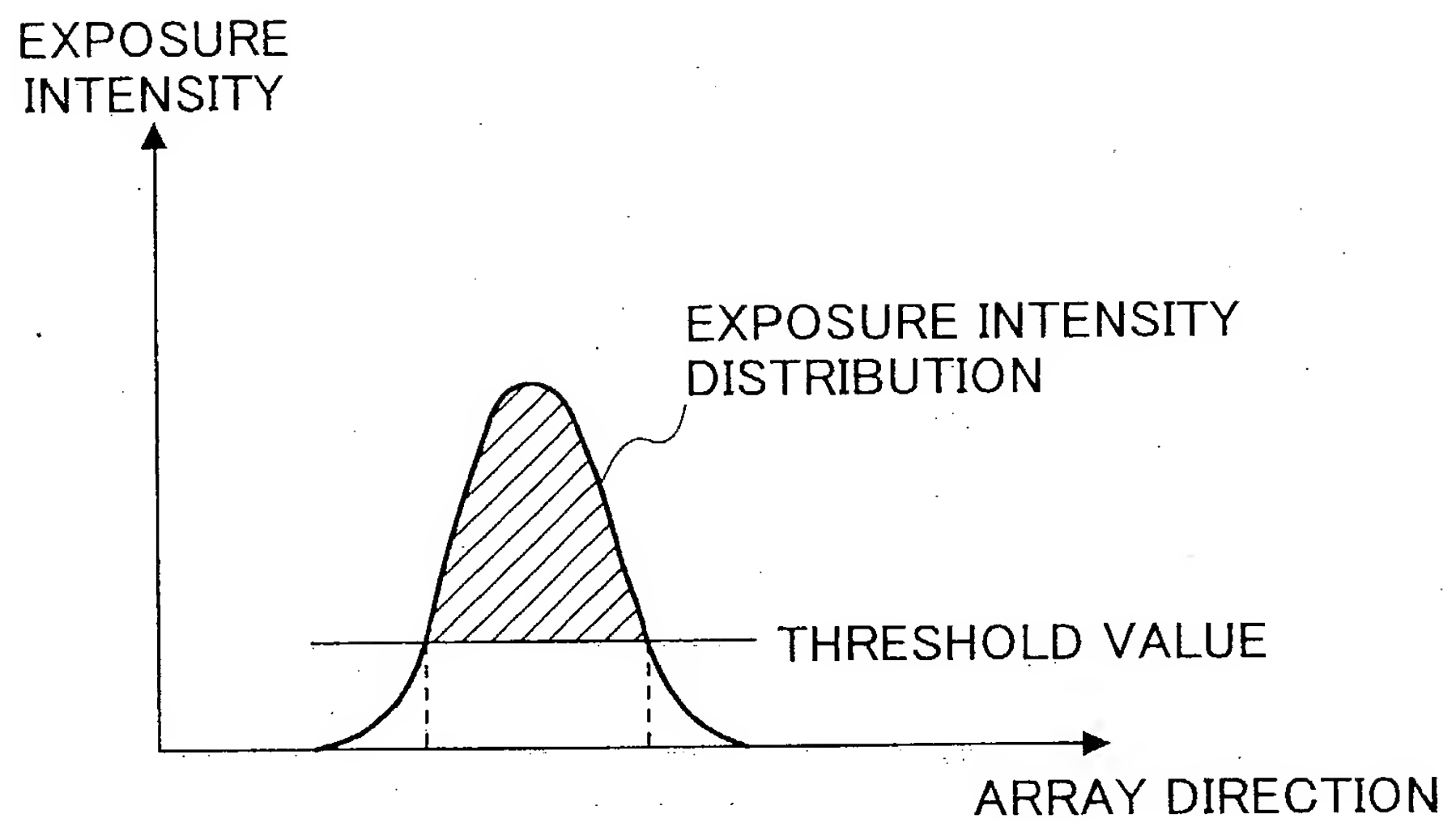


FIG.12

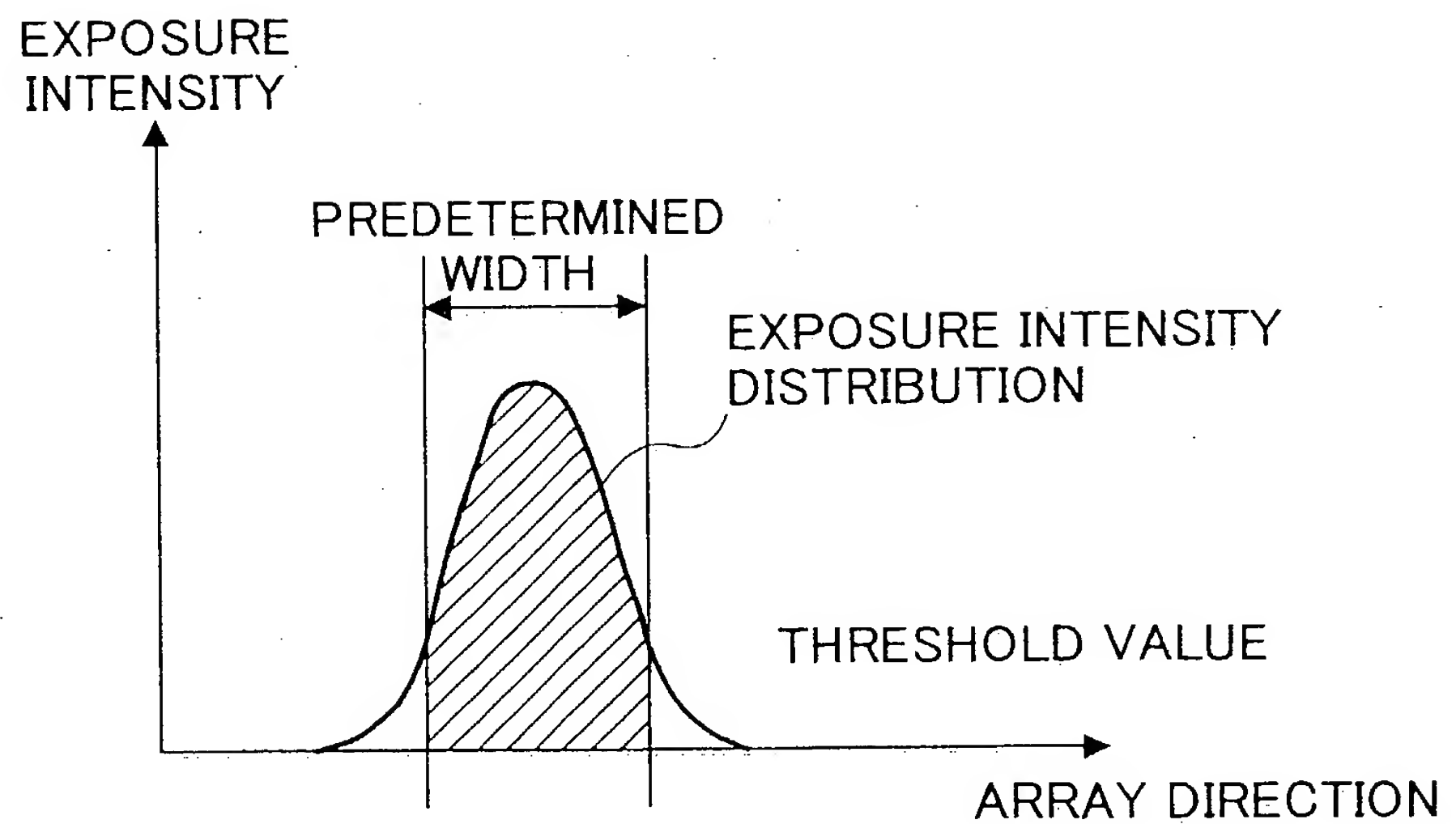


FIG.13

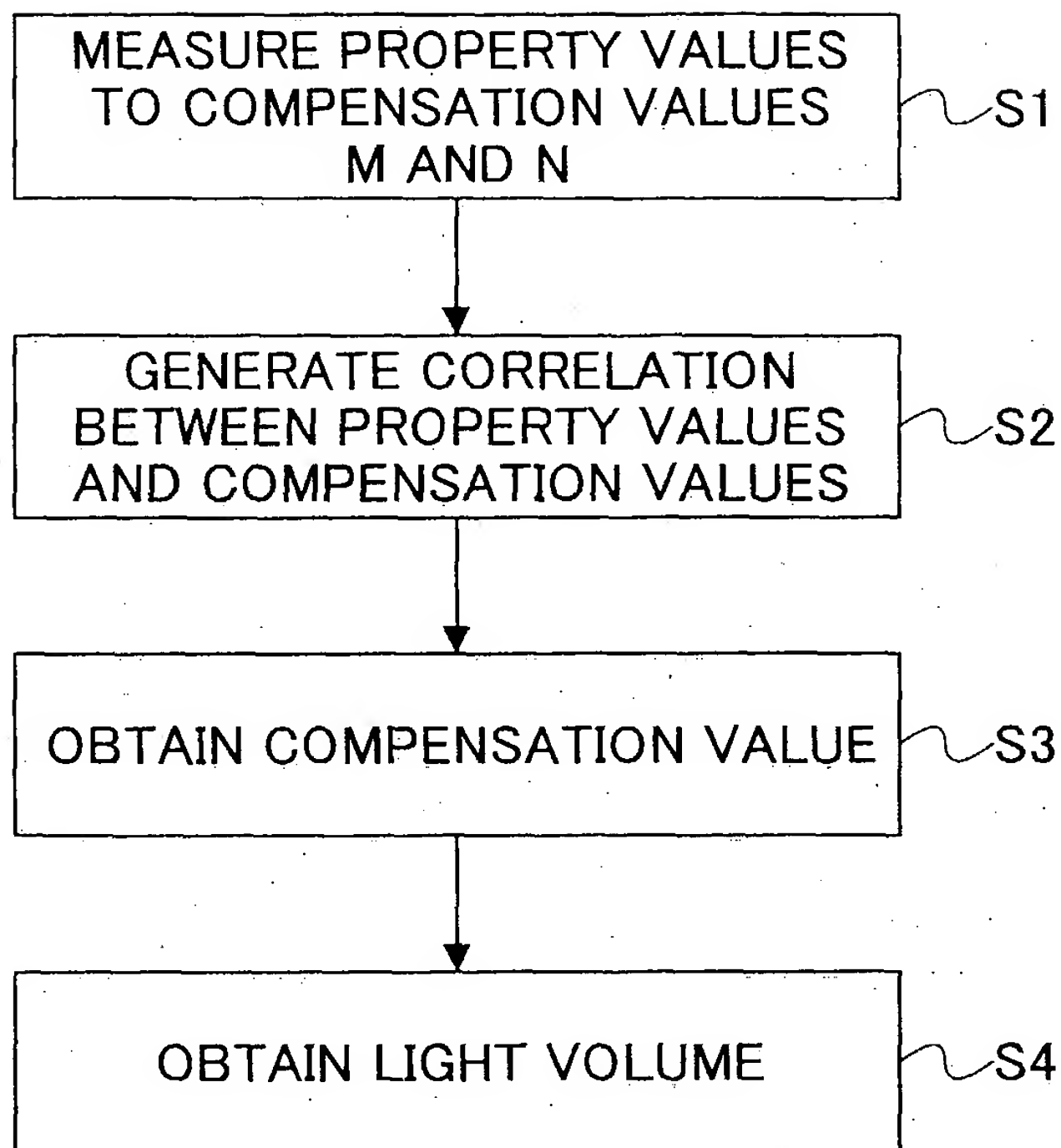


FIG.14

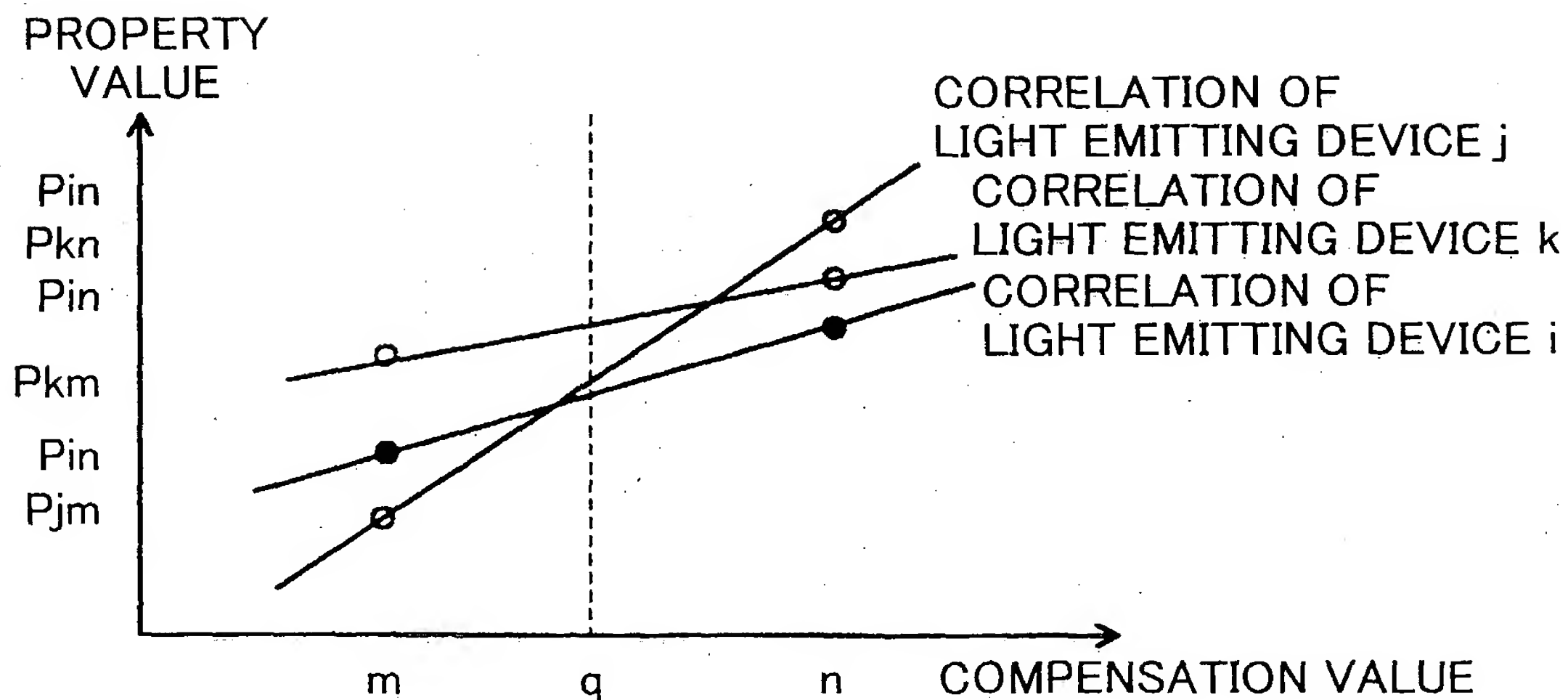


FIG.15

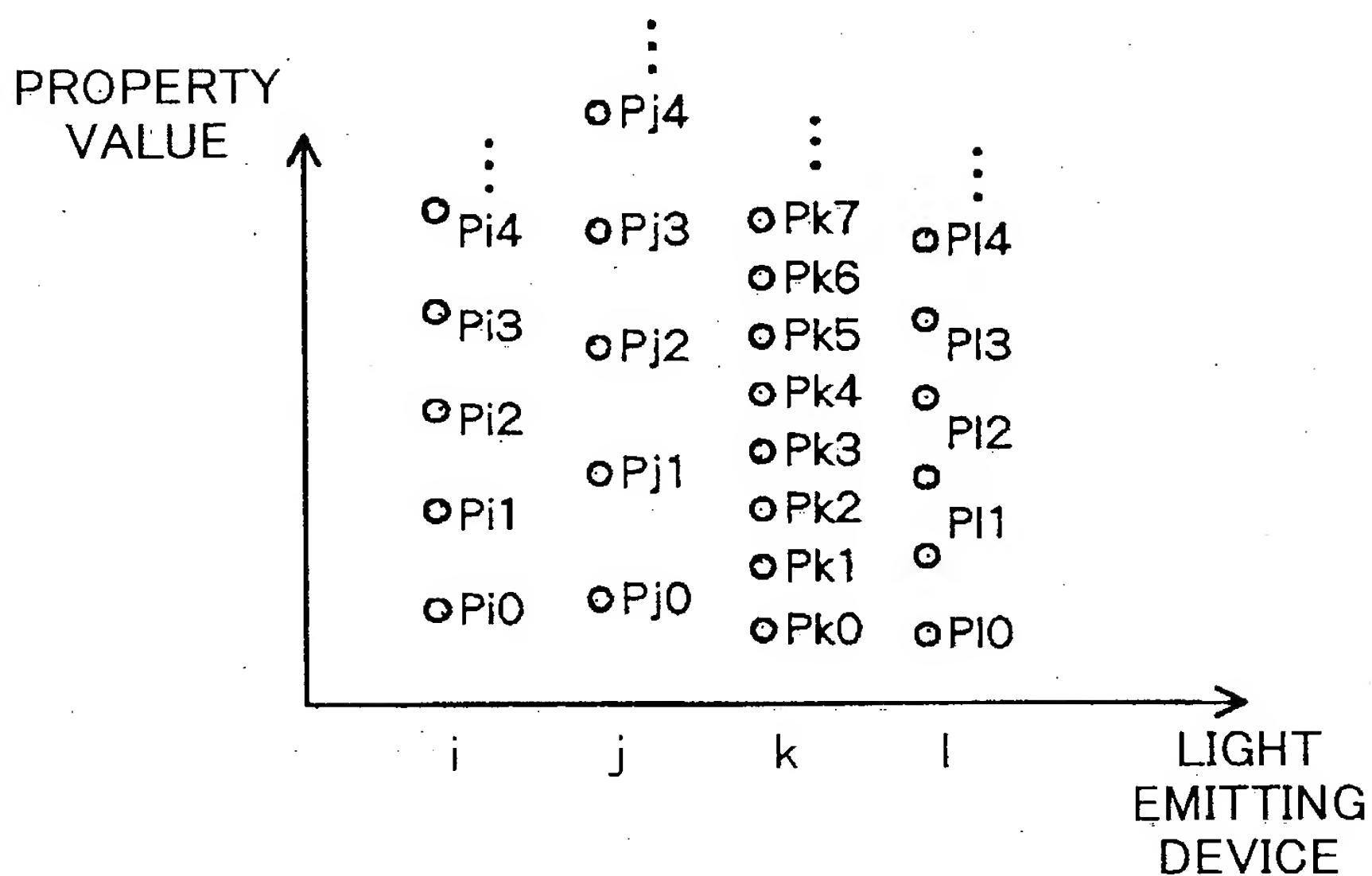


FIG.16

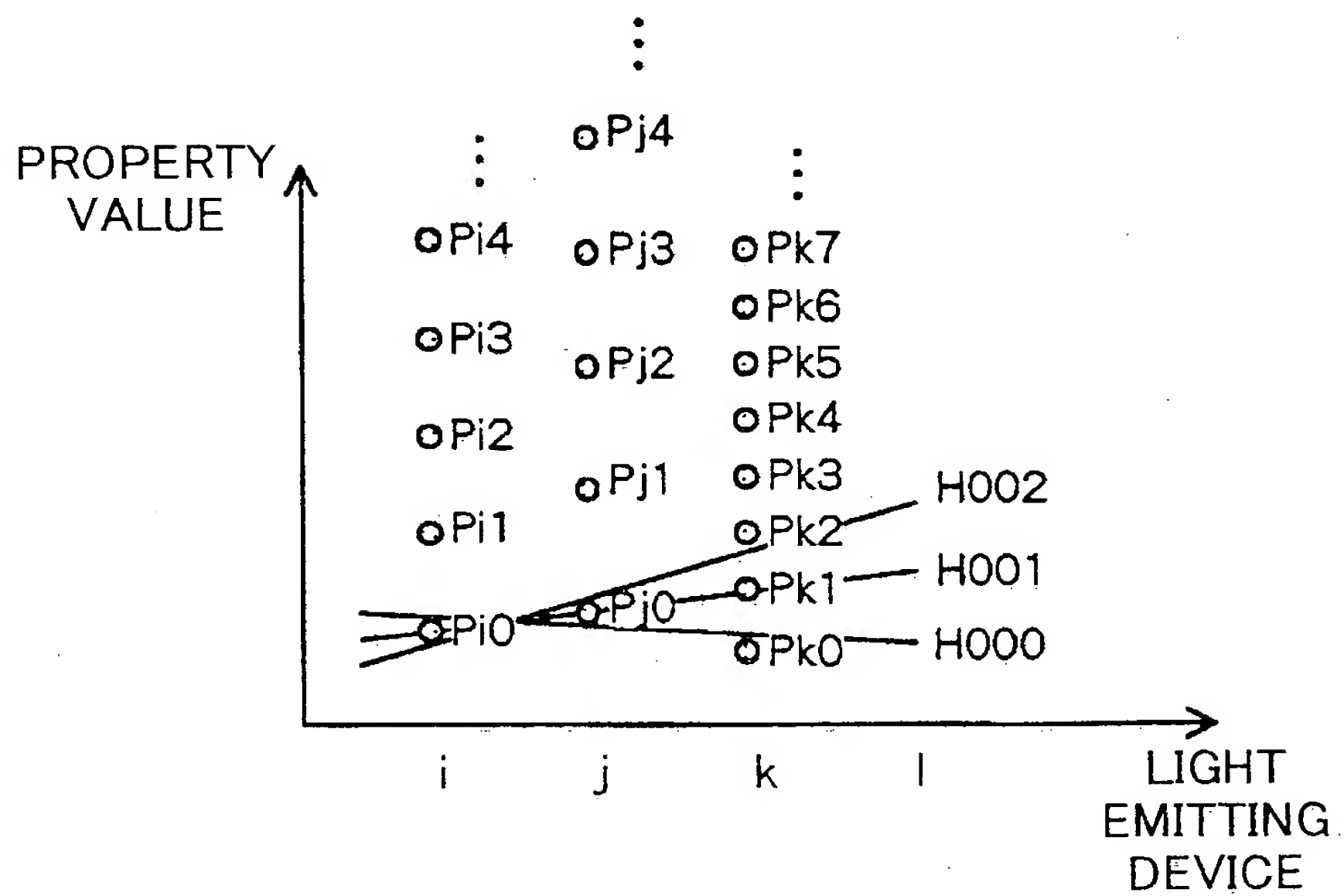


FIG.17

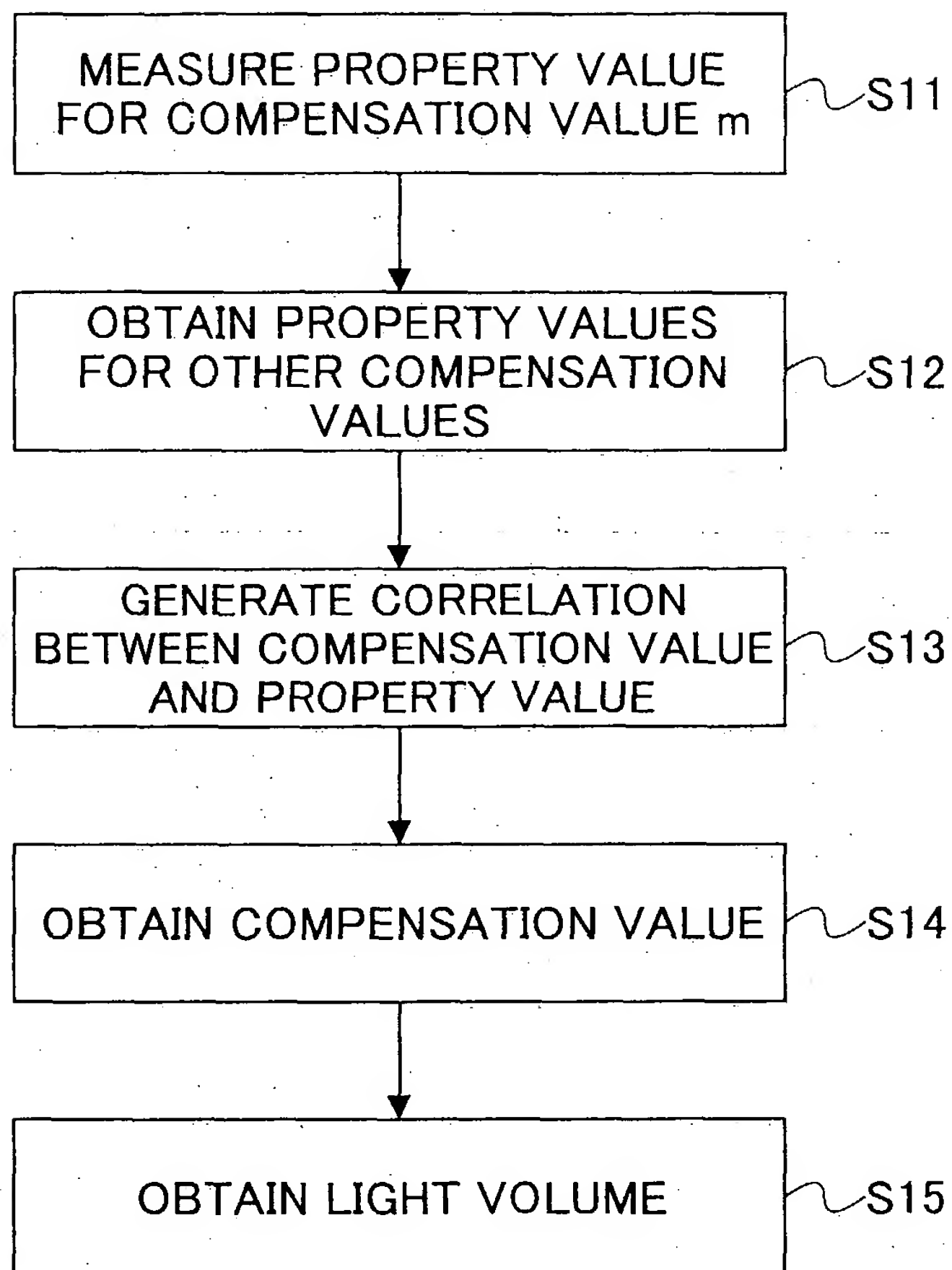


FIG.18

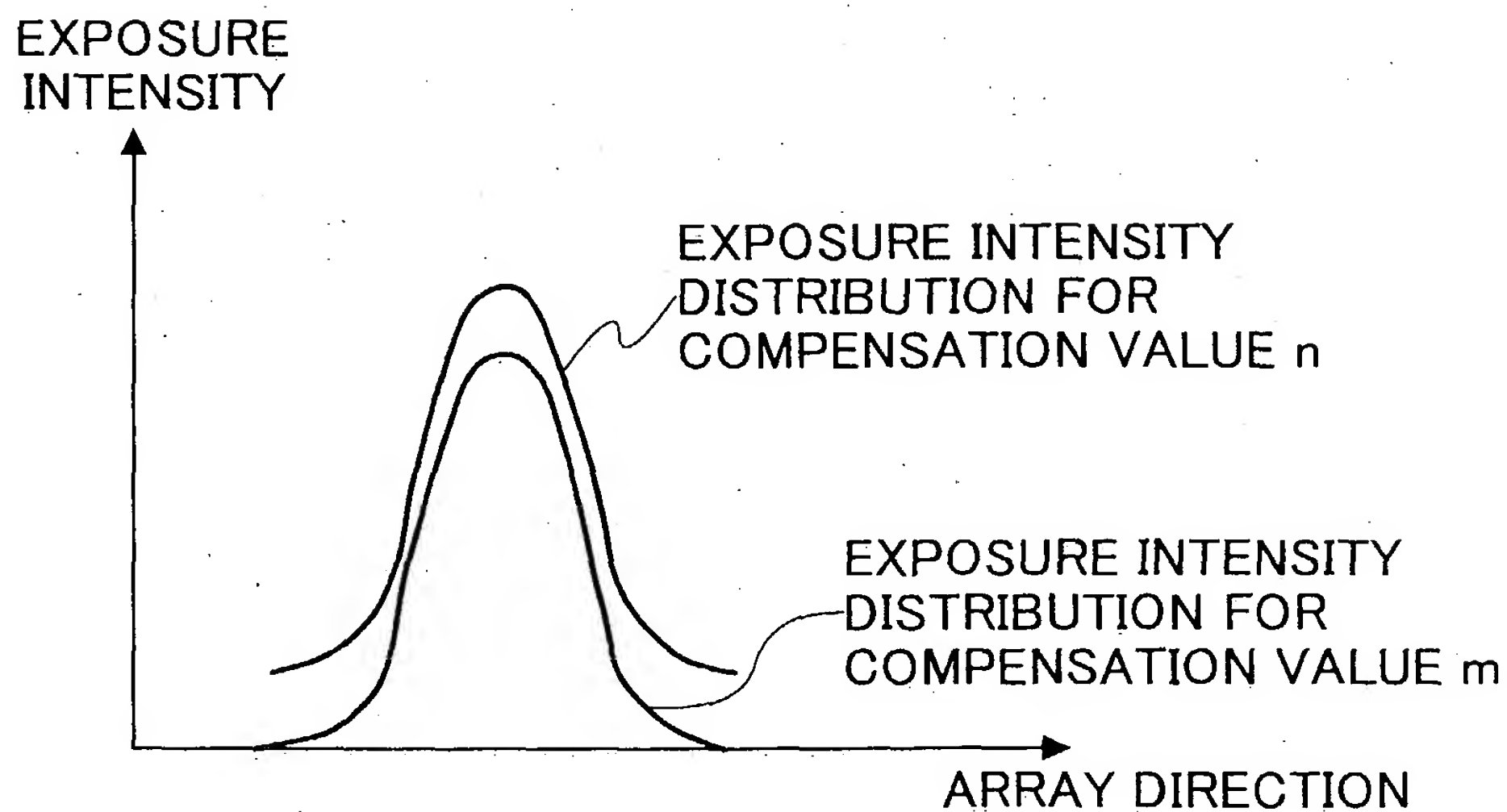


FIG.19

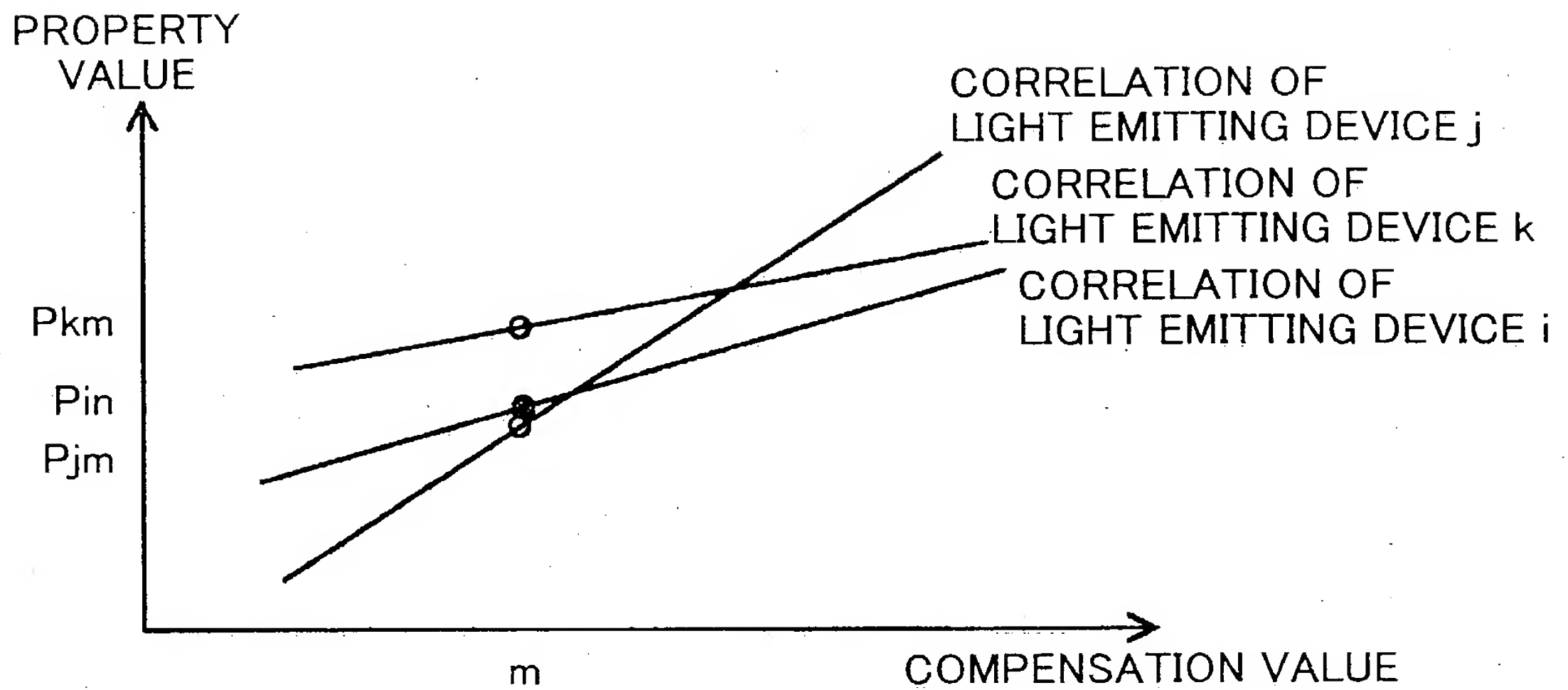


FIG.20

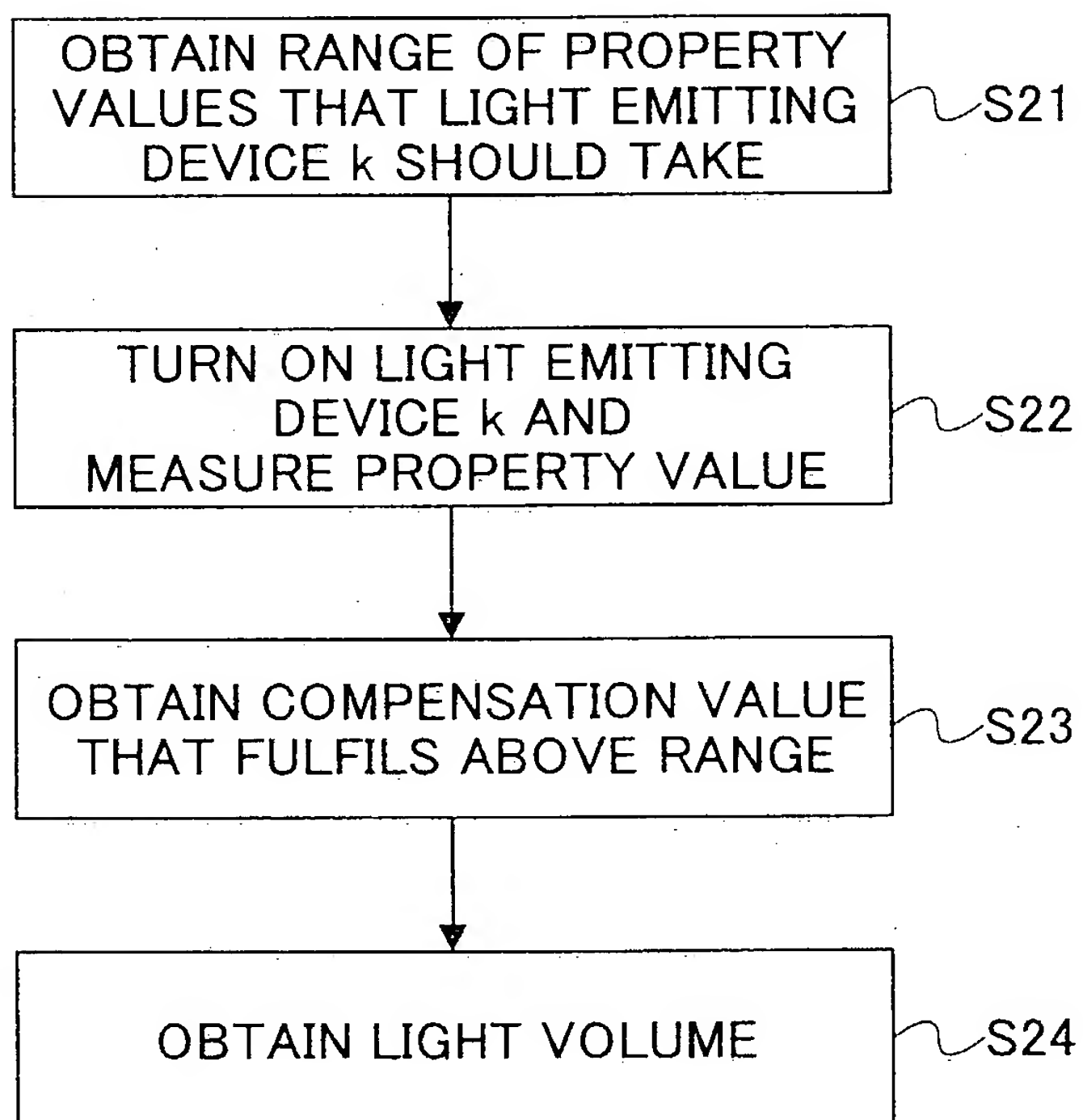


FIG.21

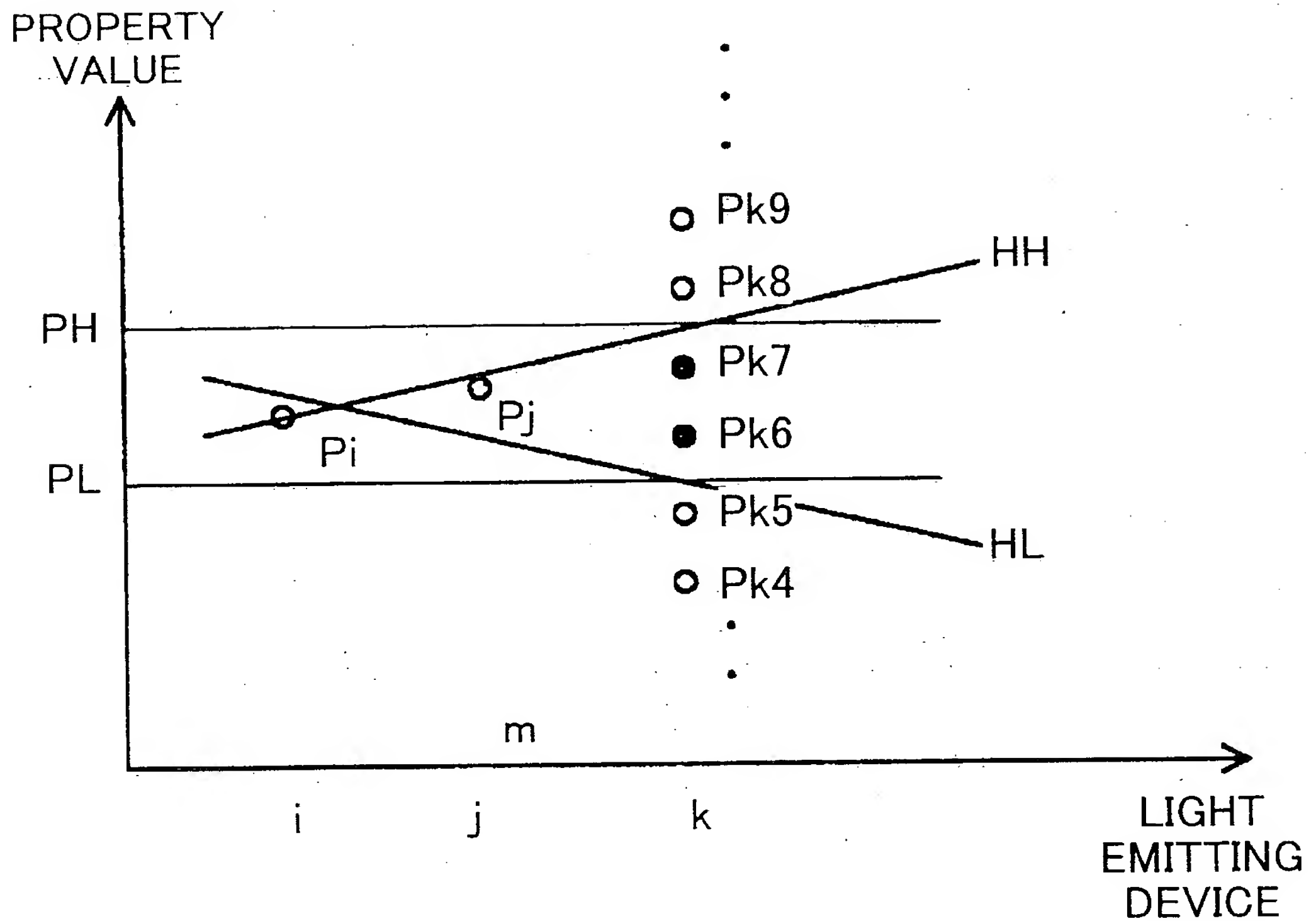


FIG.22

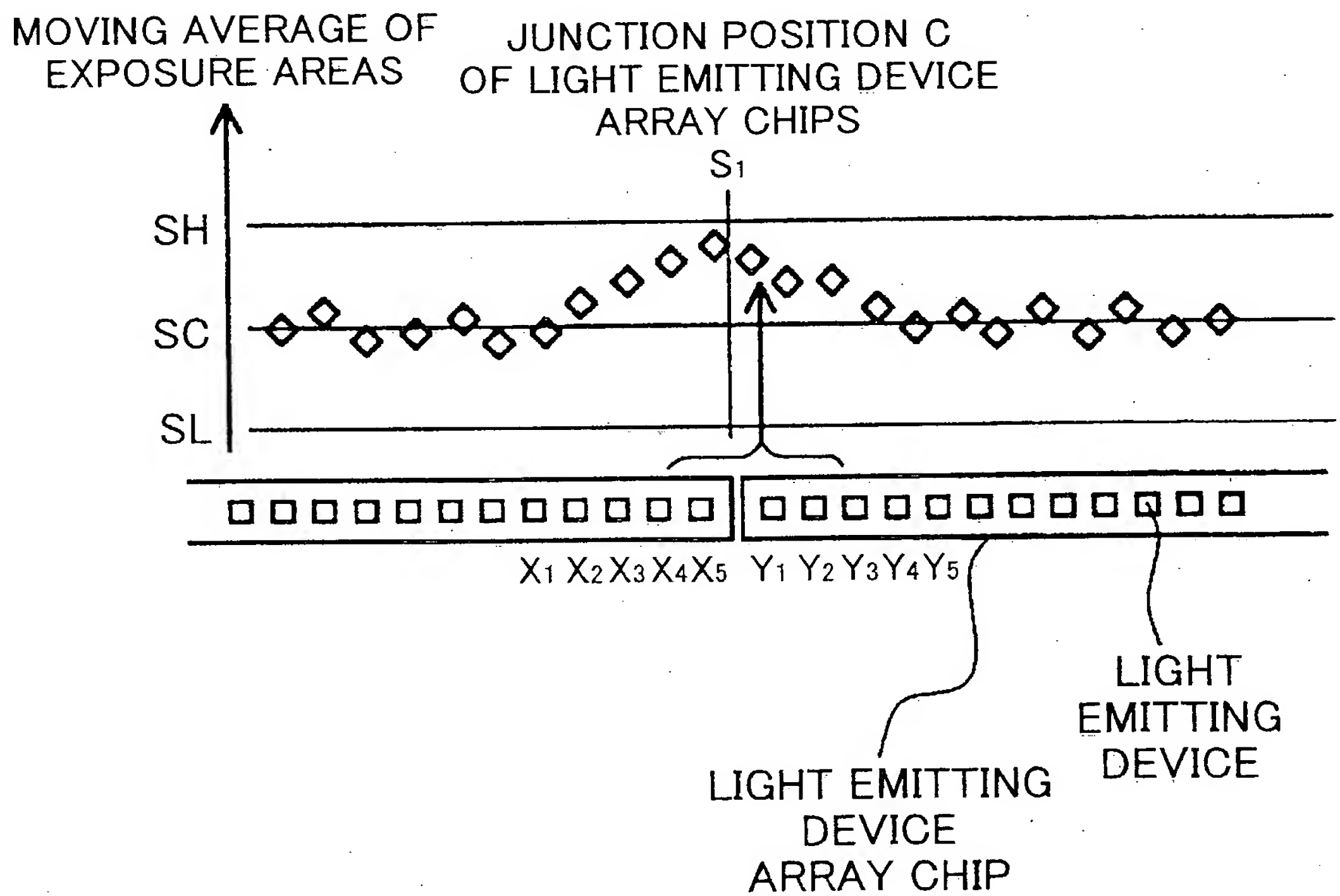


FIG. 23

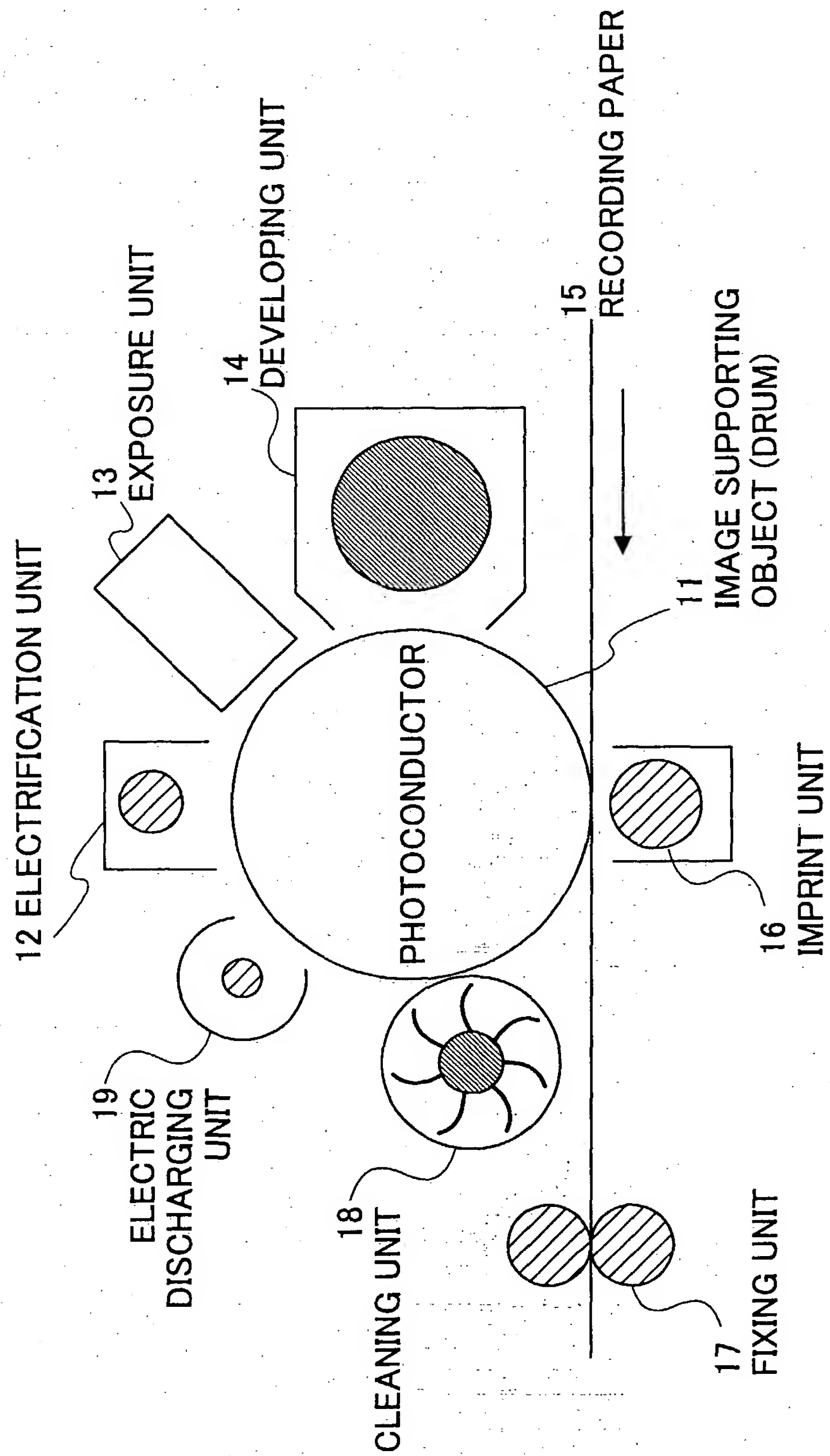


FIG.24

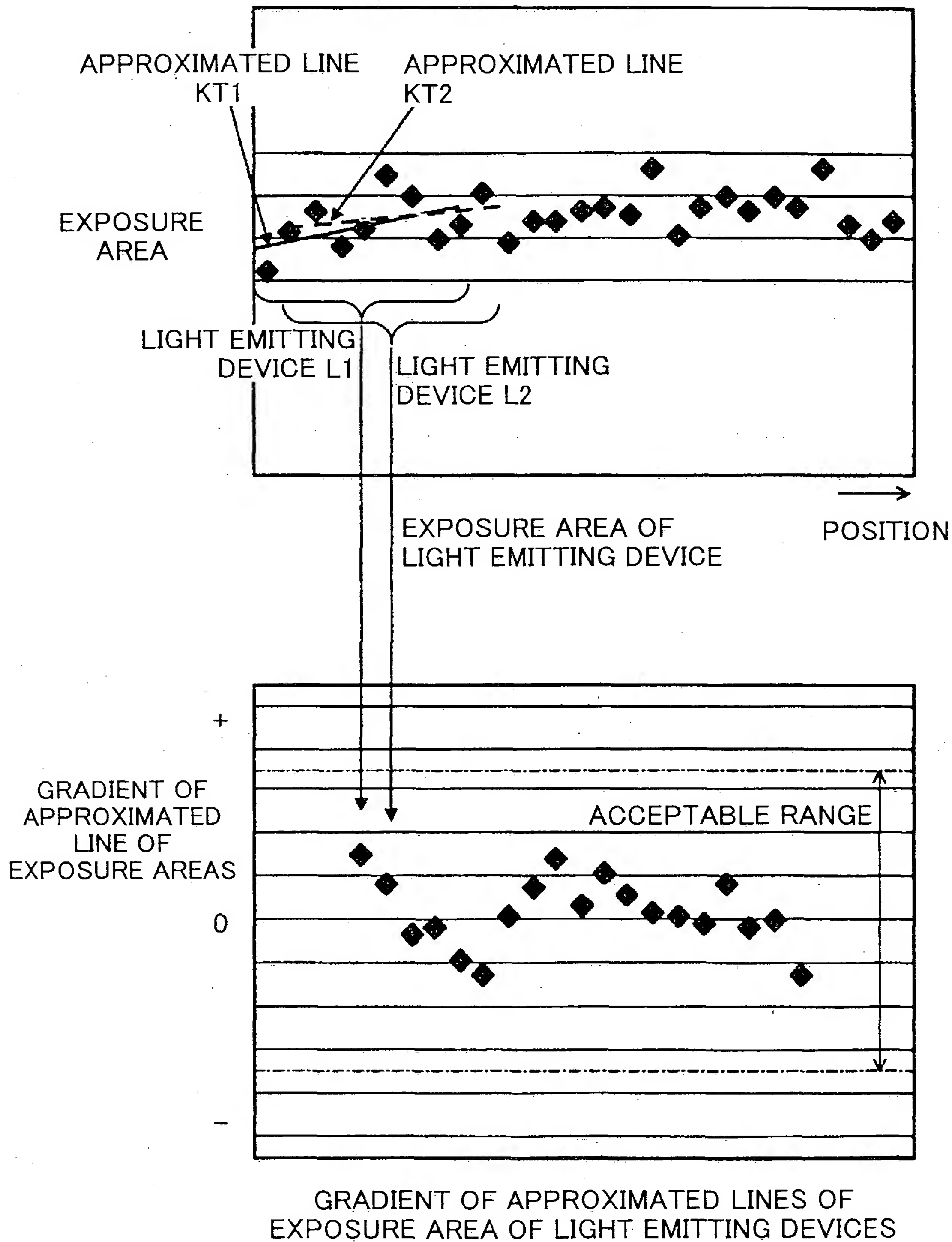
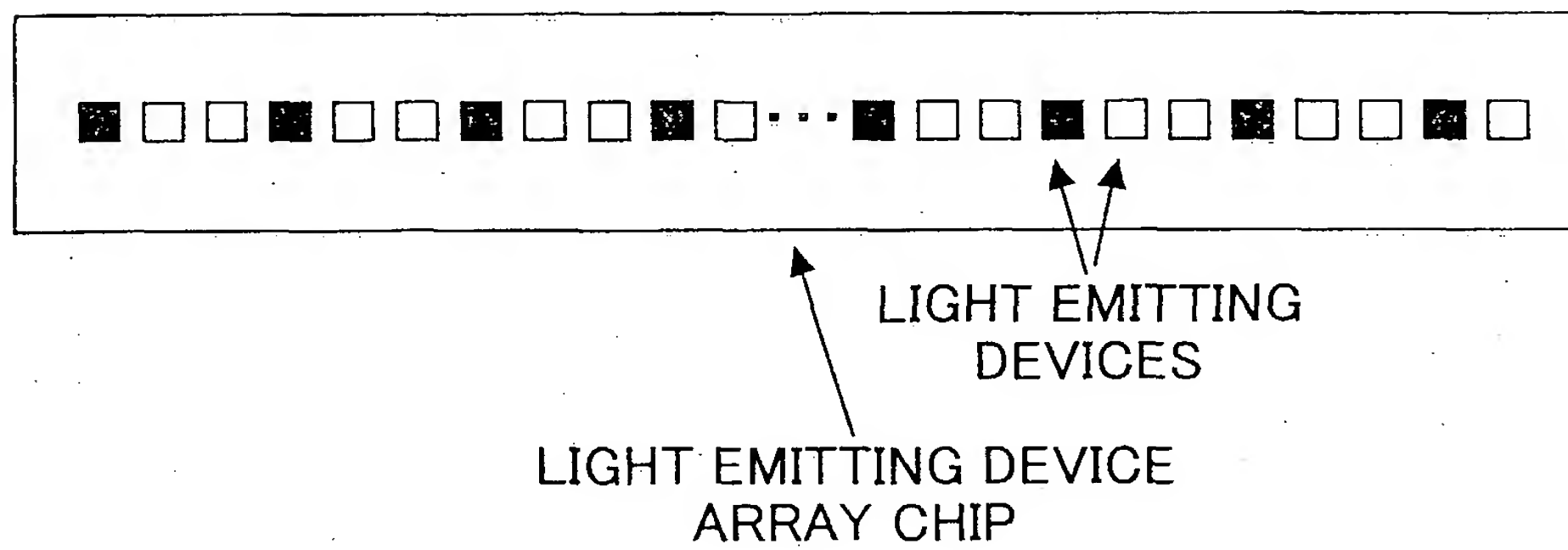
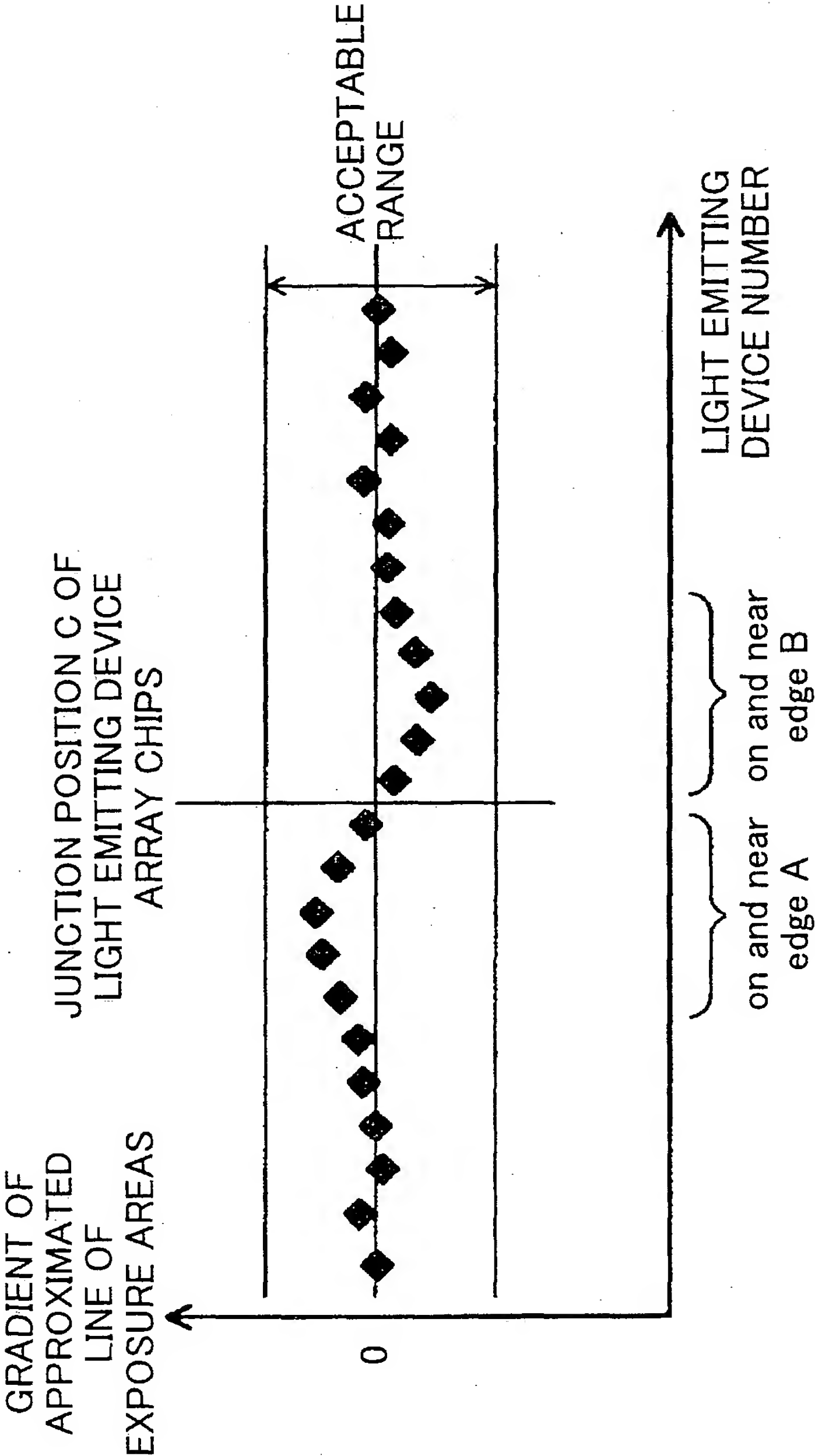


FIG.25



EVERY THIRD LIGHT EMITTING DEVICES SELECTED

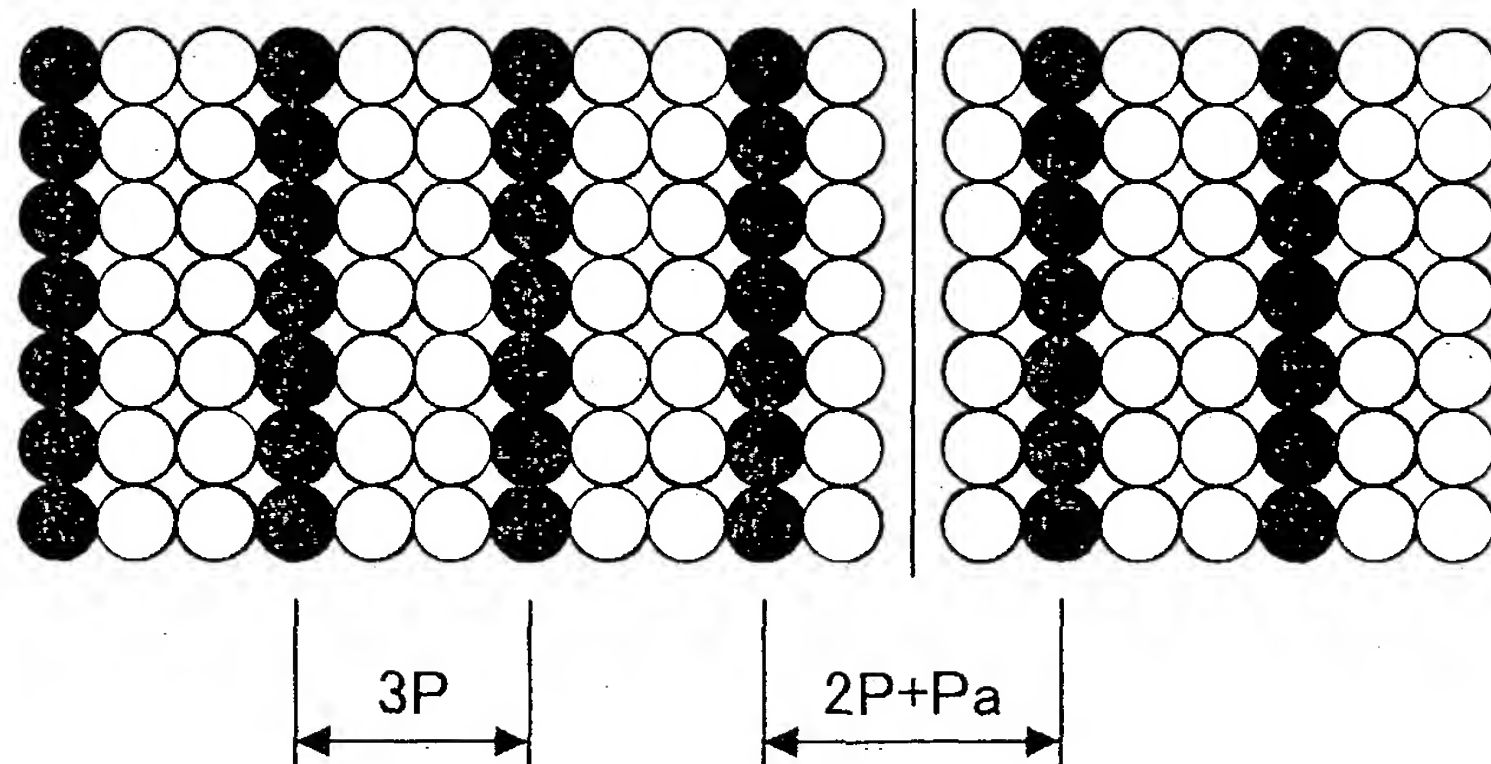
FIG.26



WHERE $P_a > P$

FIG.27

JUNCTION POSITION C
OF LIGHT EMITTING DEVICE
ARRAY CHIPS



WHERE $3P < 2P+P_a$

FIG.28

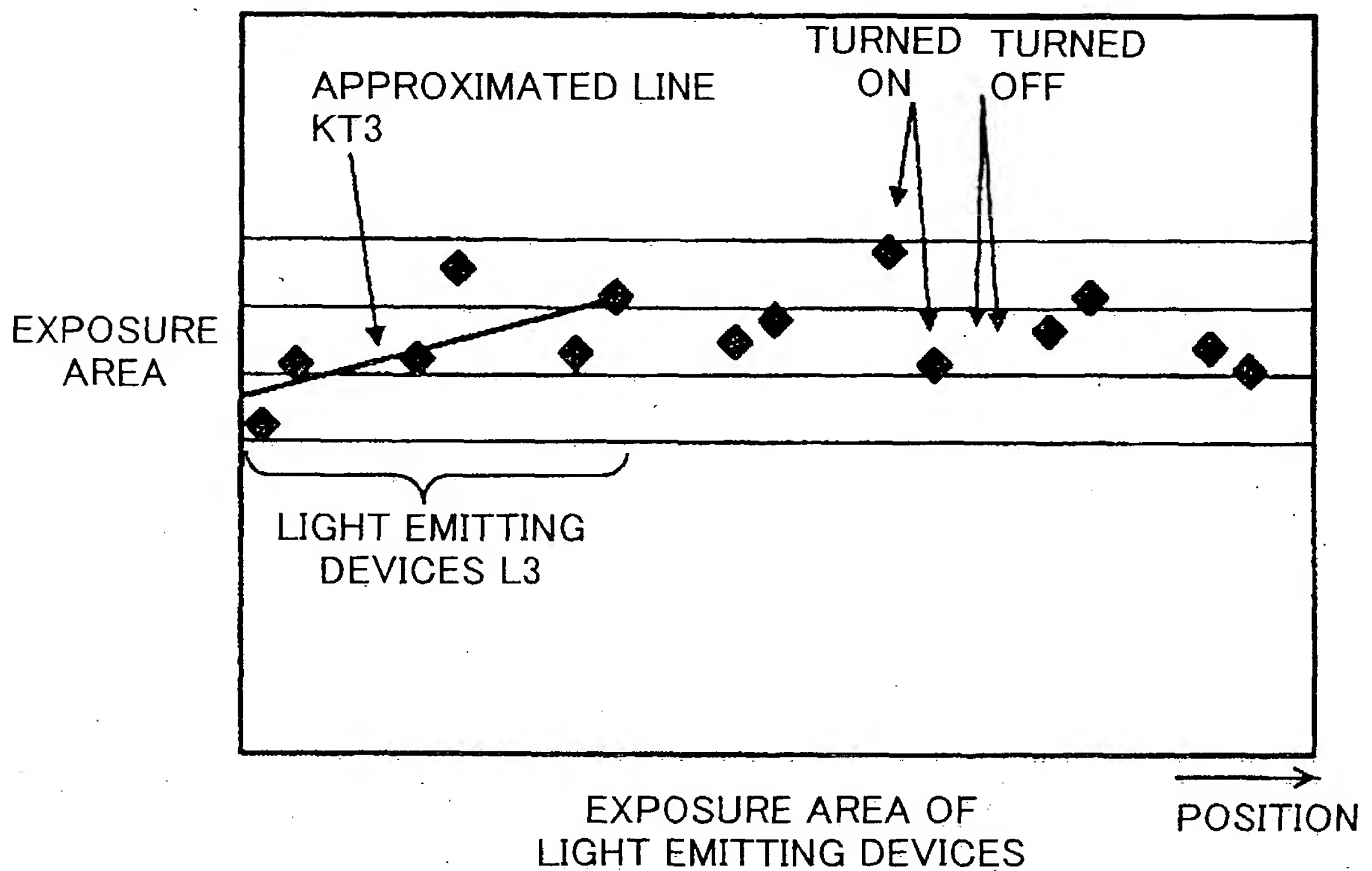


FIG.29A

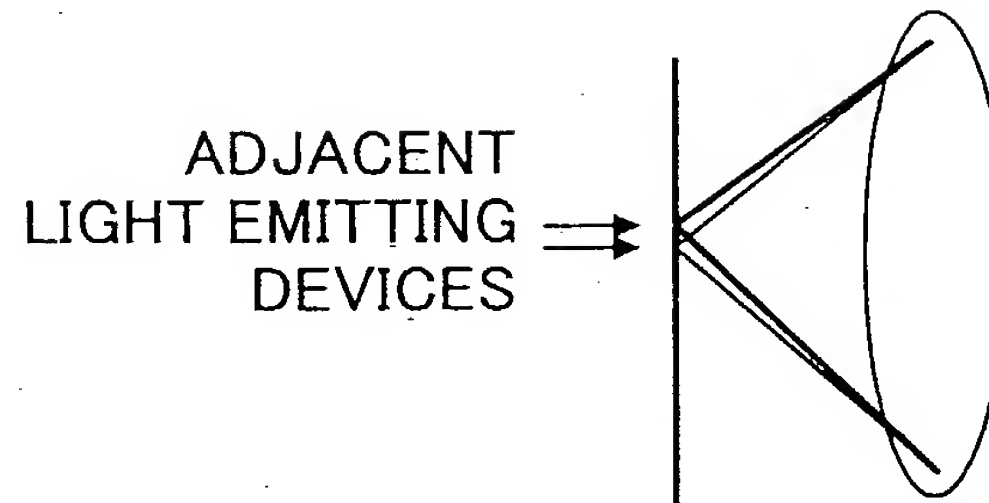


FIG.29B

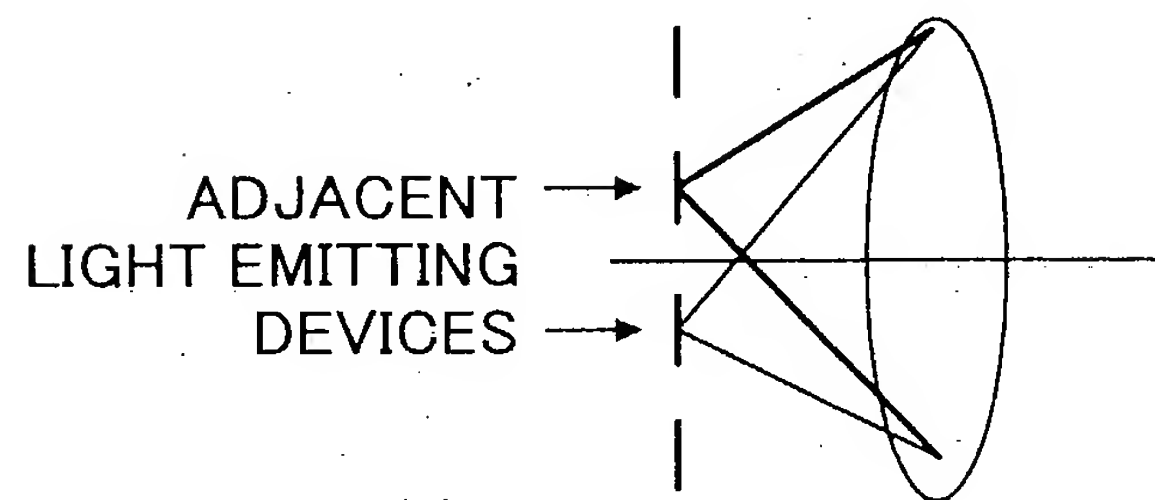


FIG.30

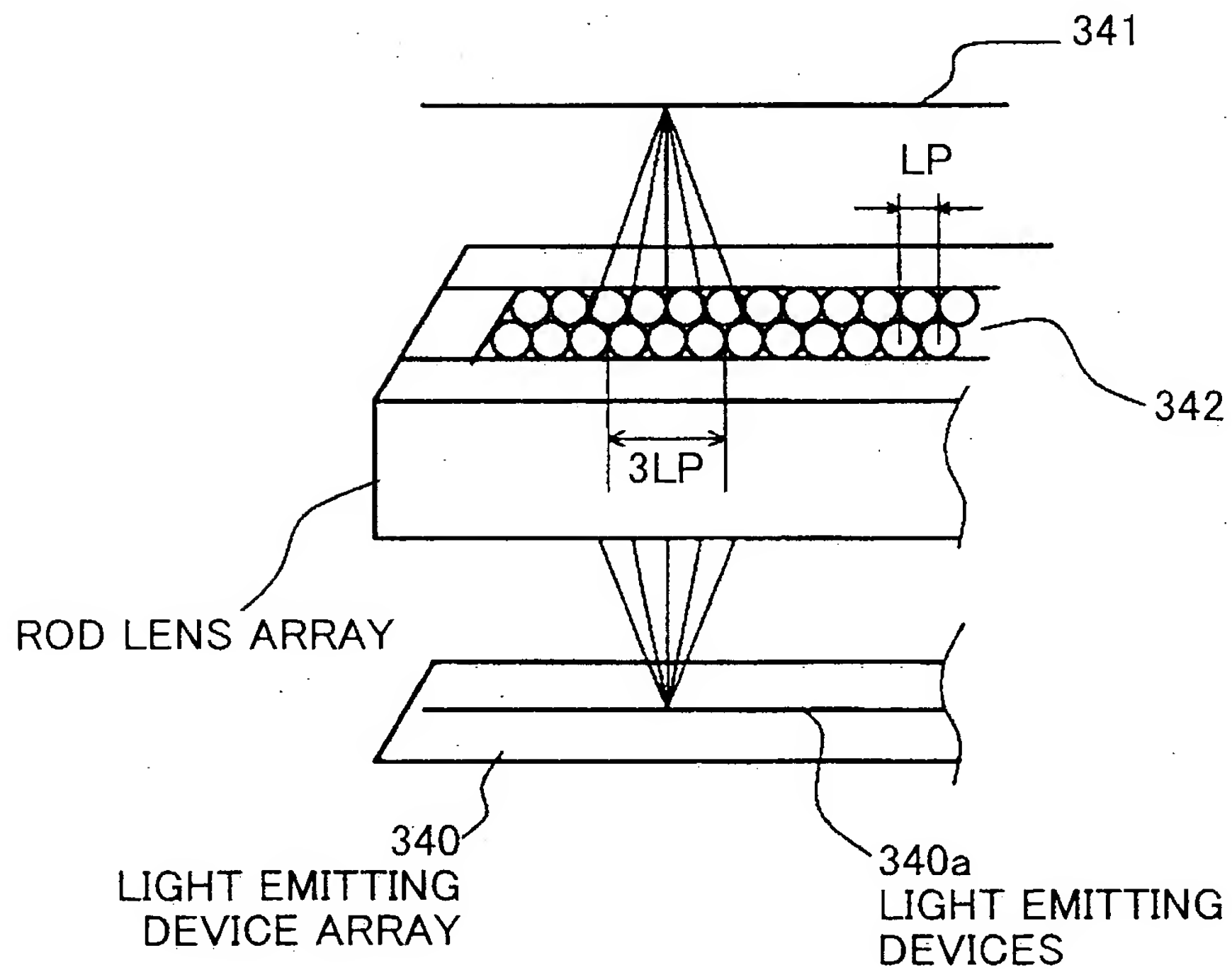


FIG.31

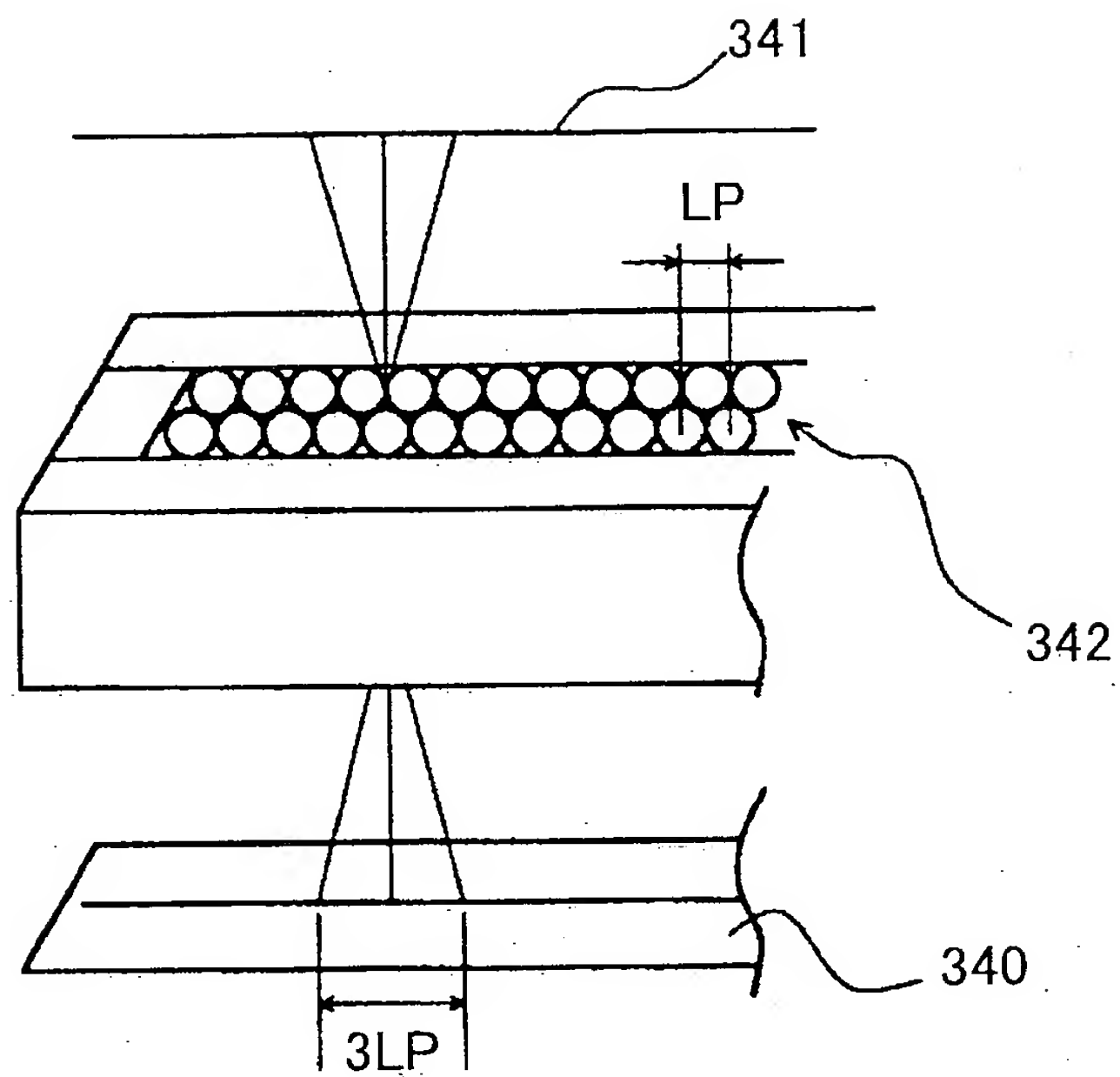


FIG. 32

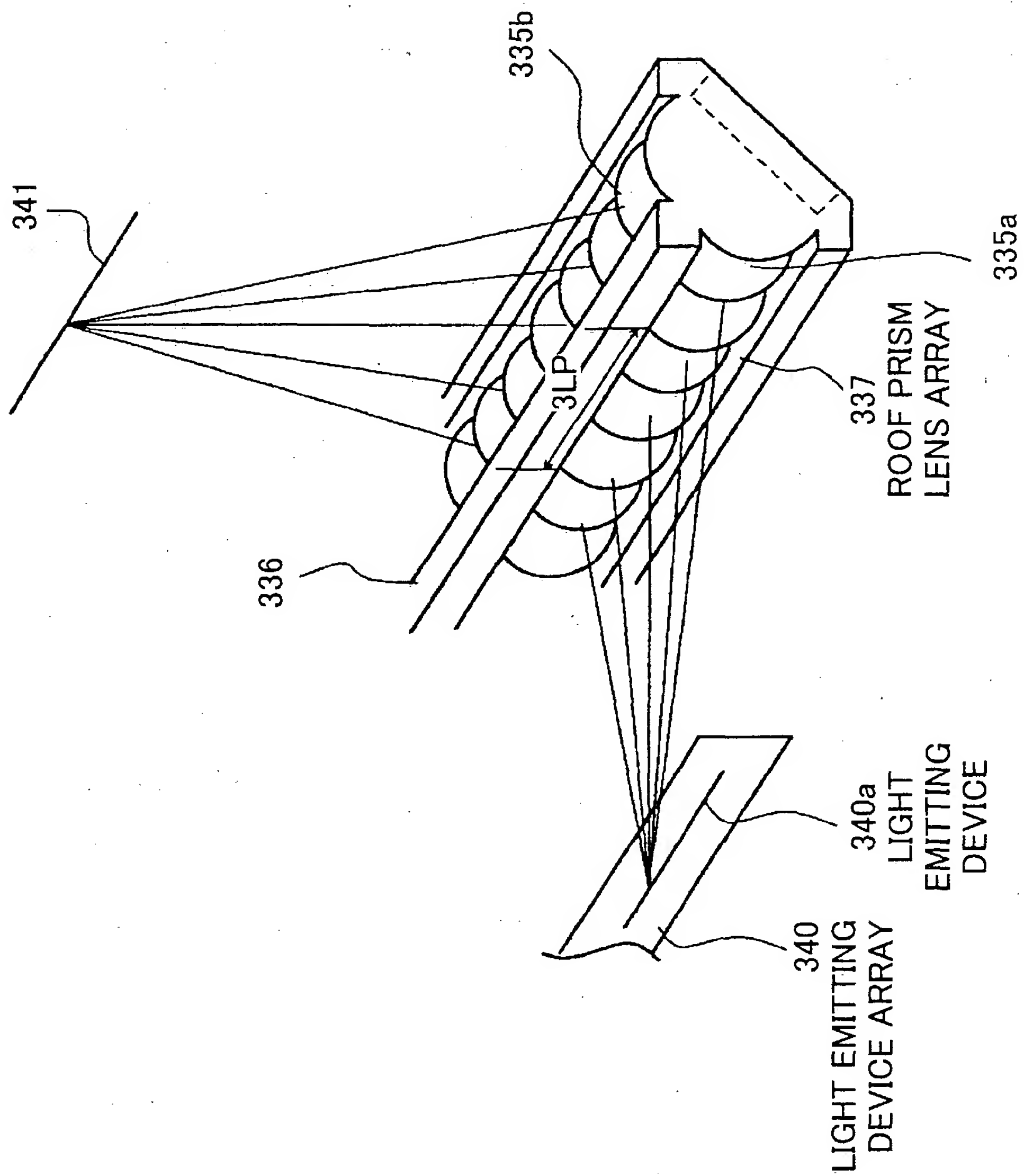


FIG.33

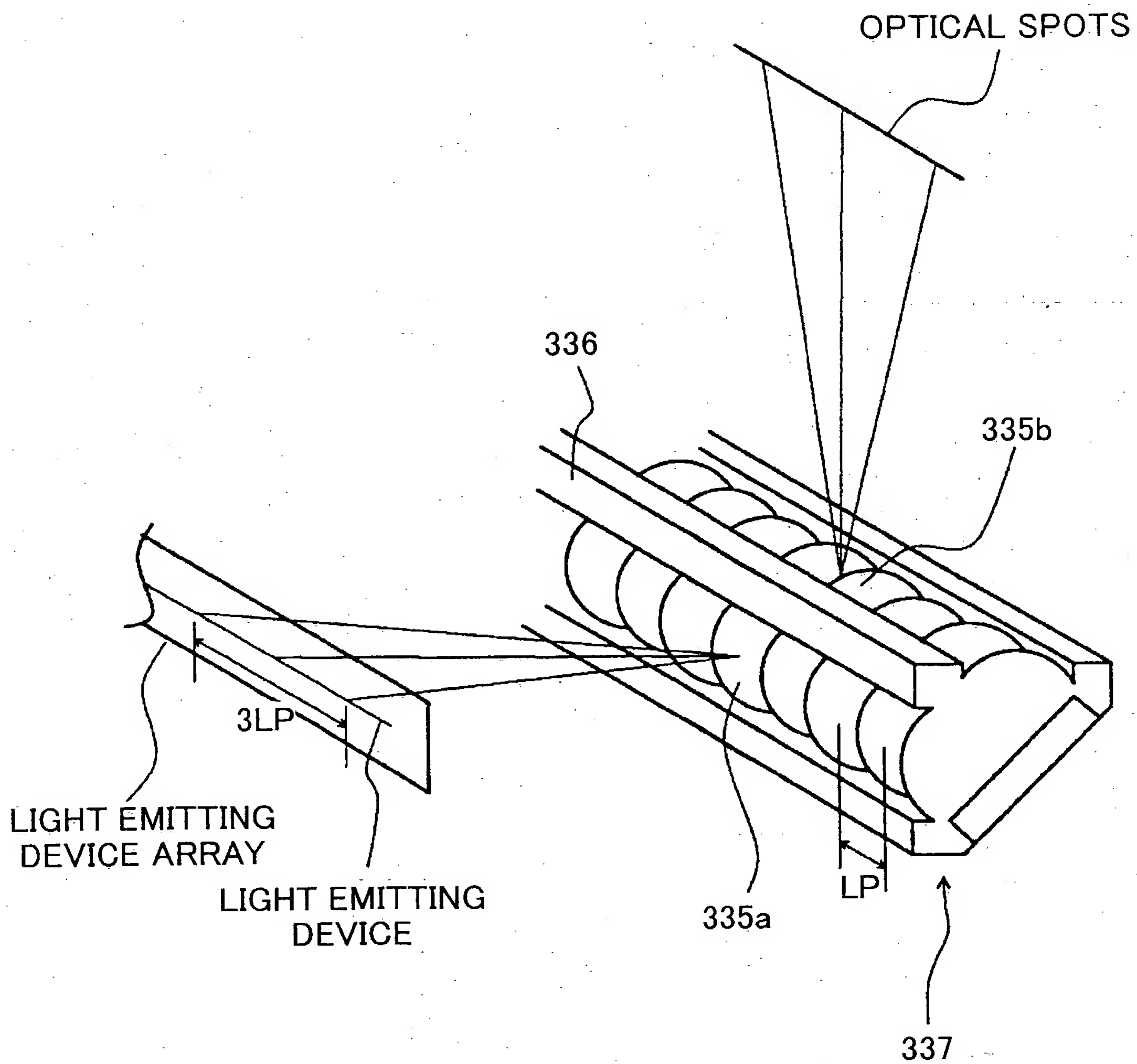


FIG.34

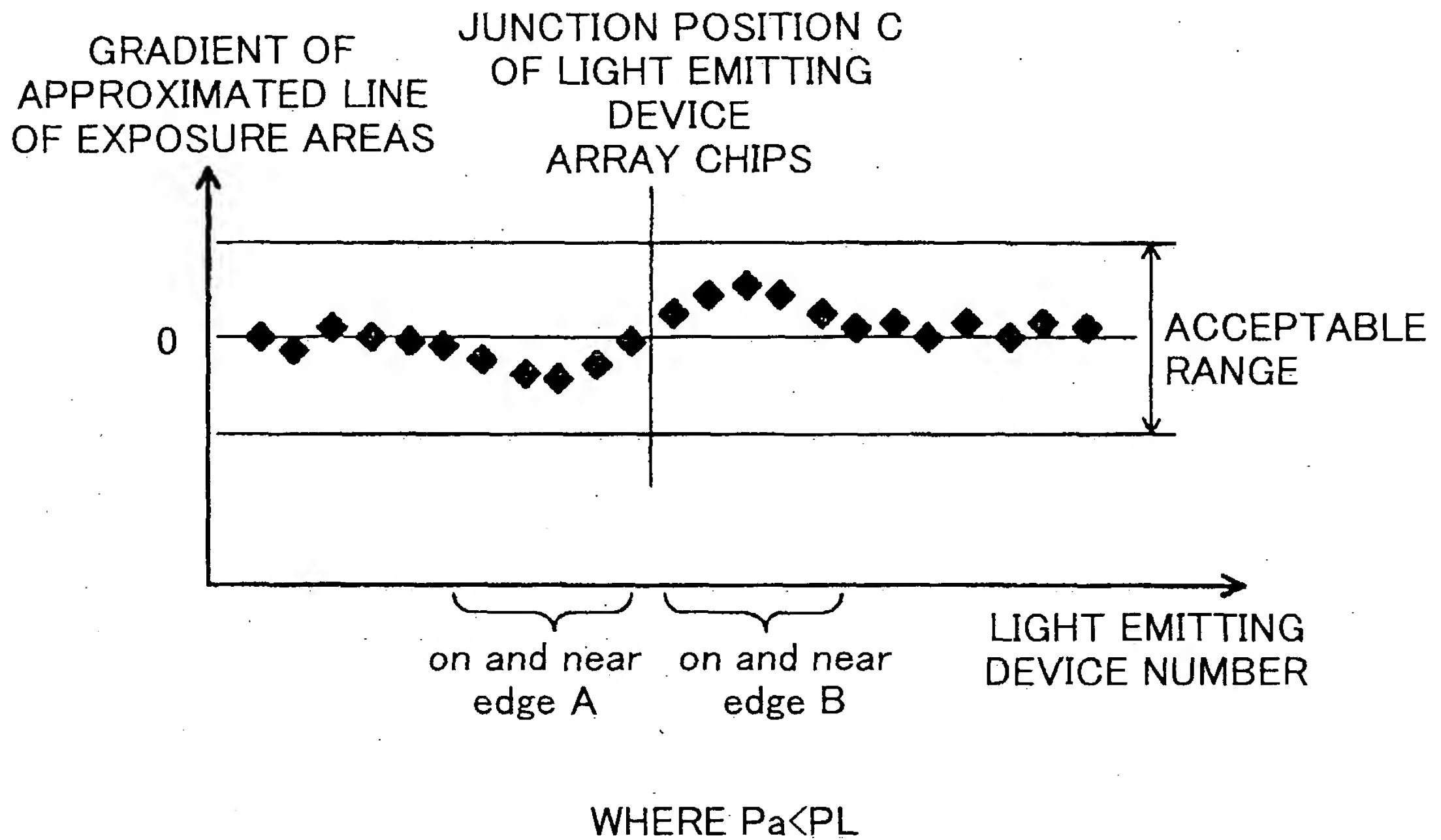


FIG.35

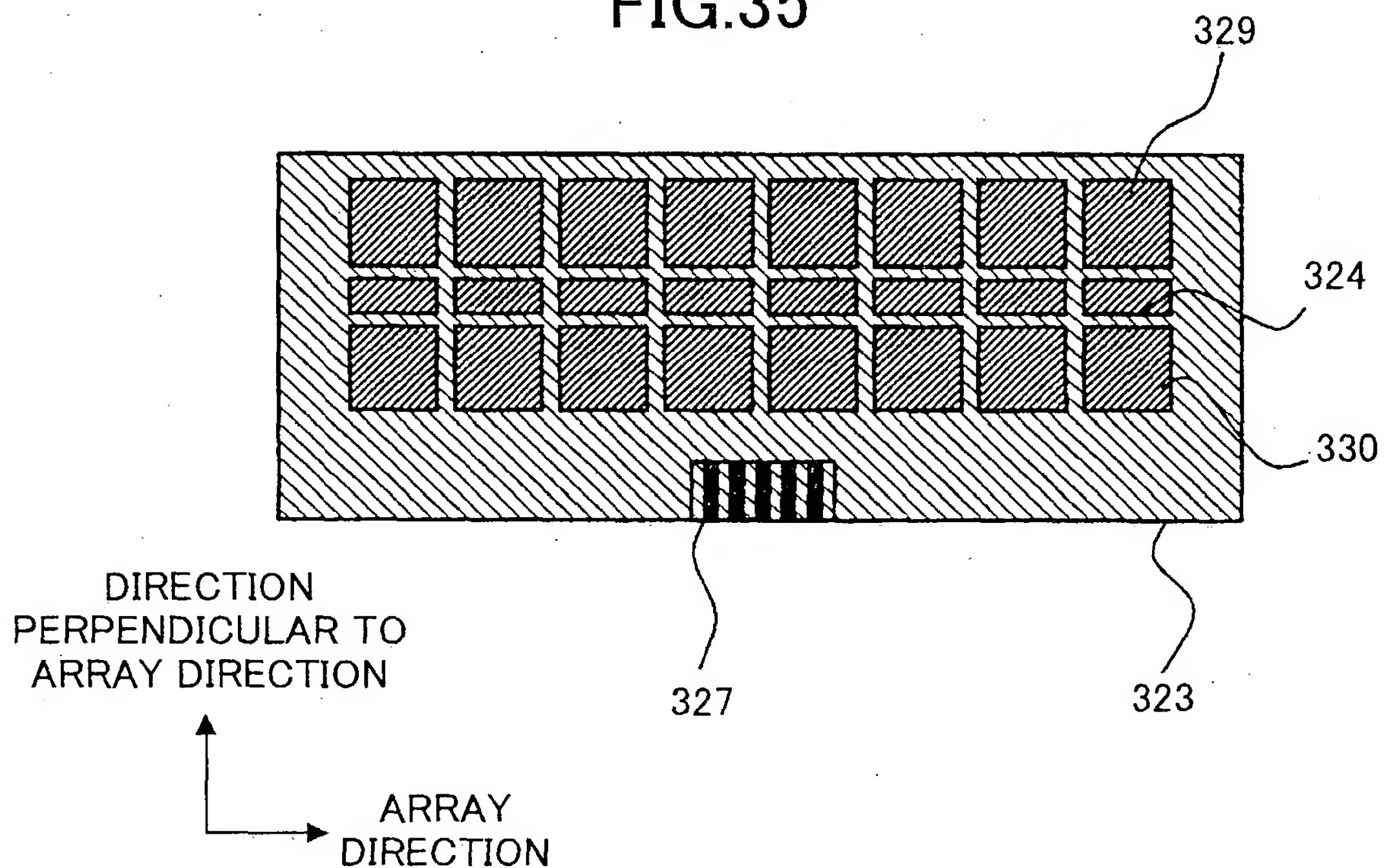


FIG.36

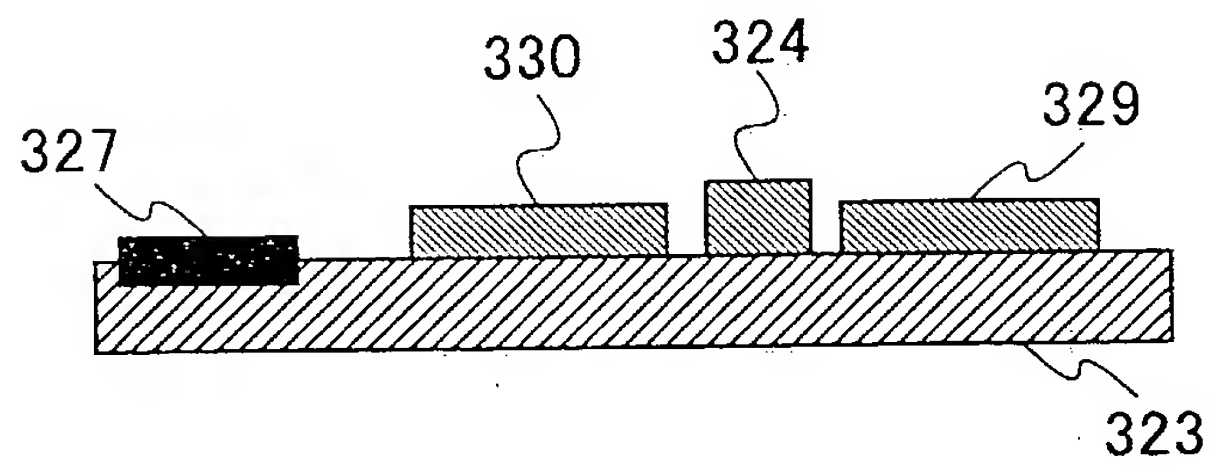


FIG.37

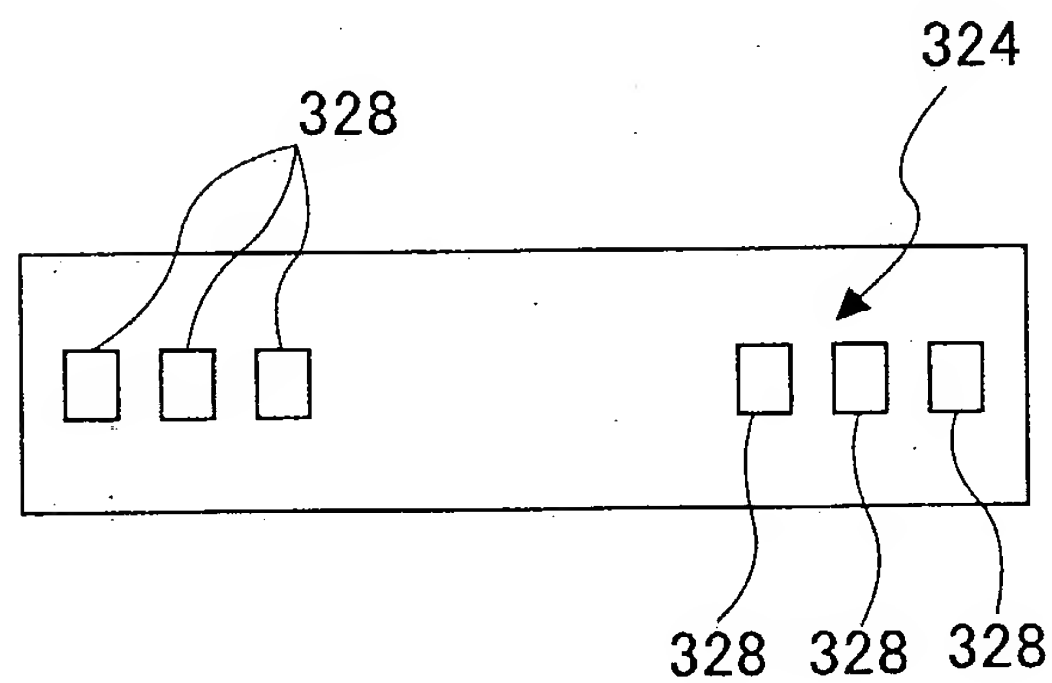


FIG.38

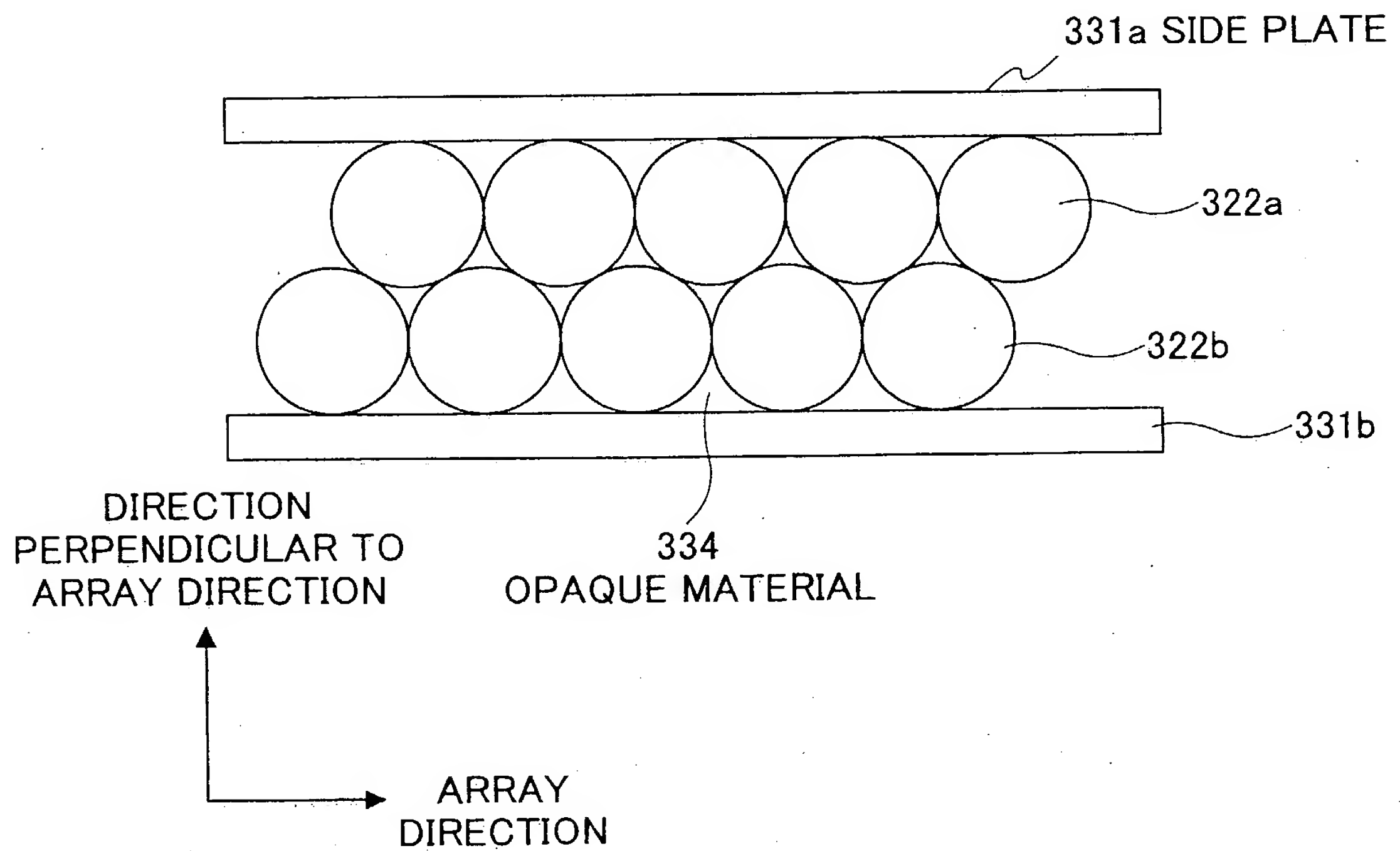


FIG.39

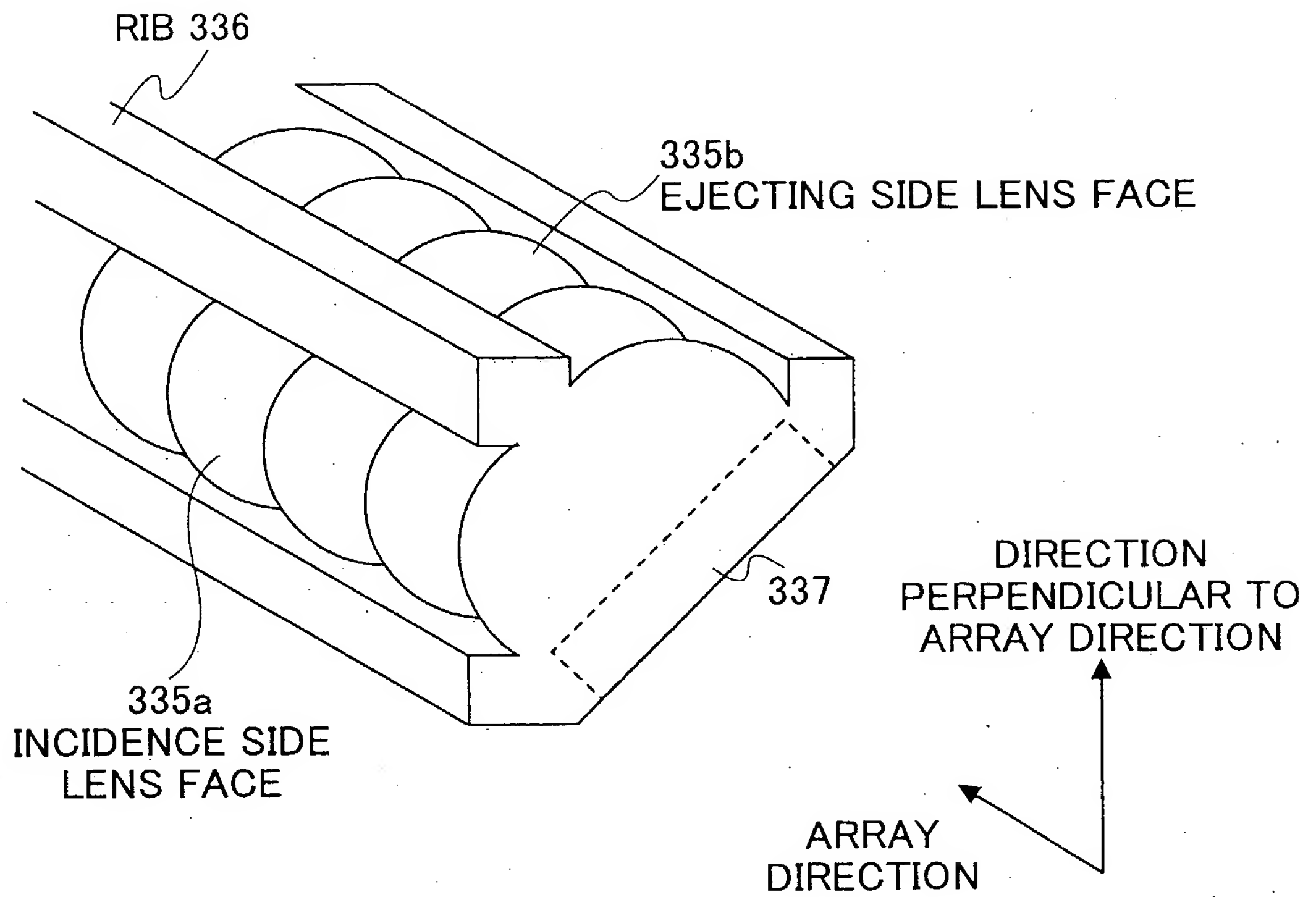


FIG.40

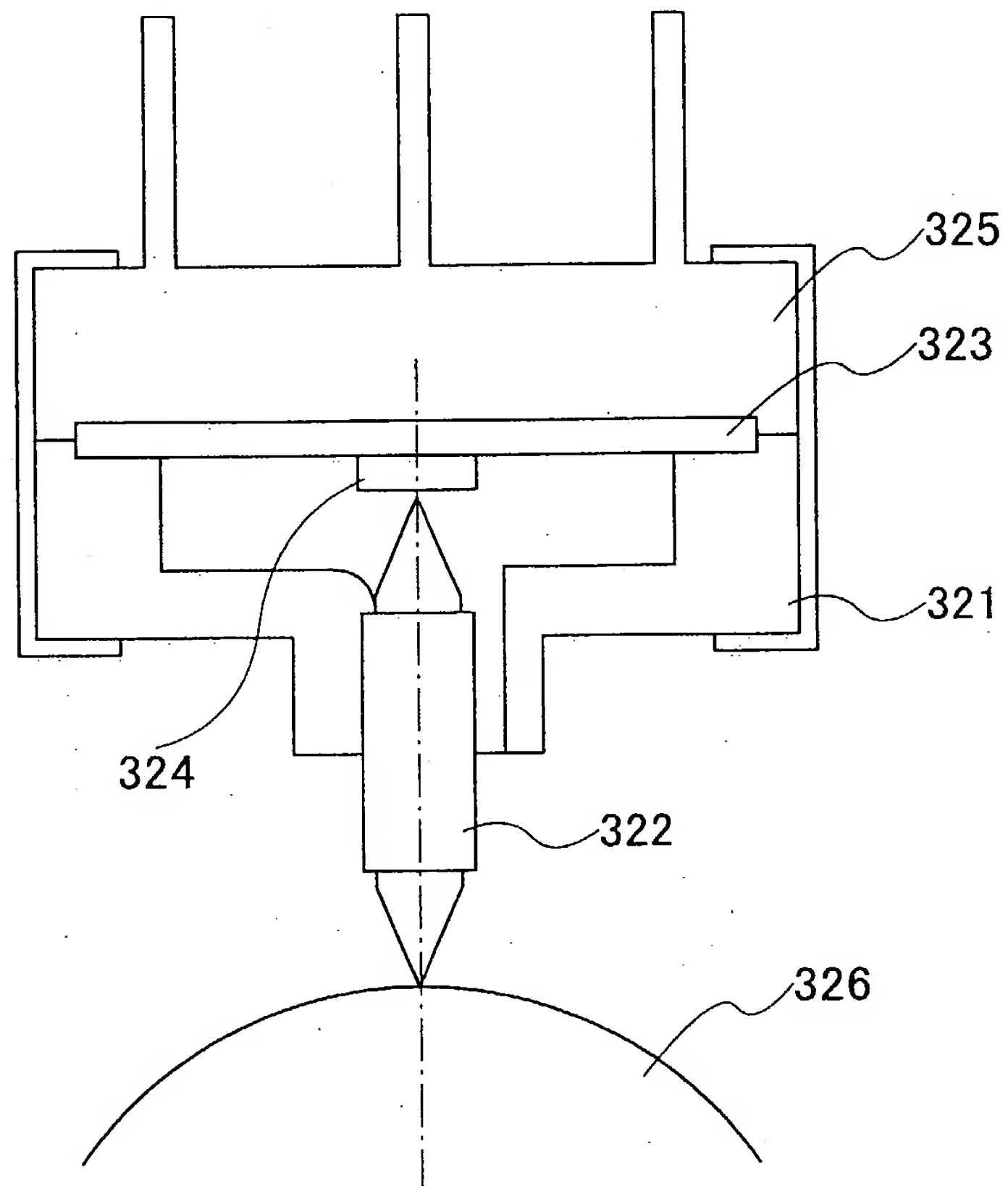


FIG.41

